

Shri Vile Parle Kelavani Mandal's
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC
NATAKKAR RAM GANESH GADKARI MARG
VILE PARLE (WEST), MUMBAI-400056

CURRICULUM
DIPLOMA COURSE IN DIGITAL ELECTRONICS

SHRI BHAGUBHAI MAFATLAL POLYTECHNIC started 3 years' diploma courses and was affiliated to the Board of Technical Examinations, Maharashtra State, in 1963. Since 1969, academic freedom was granted to the Polytechnic, which enabled it to evolve curriculum and examination scheme and to introduce inplant training, which involved industry-institute interaction. Thus 4 years' semester sandwich pattern came into existence. Since 1978-79, academic freedom was extended to all the full time diploma courses. In 1989-90 full autonomy was granted to all 7 full-time diploma courses.

As a further development to the above, the multi point entry and credit system (MPECS) was initiated in 1981 on progressive basis. In this scheme students can regulate their pace of studies within the rules prescribed.

From 1993-94, full academic autonomy was extended to all the 19 courses, which includes full-time diploma, part-time diploma and post-diploma courses. The students have to qualify for appearing in the final examinations as per details given in the MPECS rules. The examinations are conducted by the institute and the final diploma is awarded by the institute at the convocation function.

The focus of Digital Electronics course is to acquaint the students with basic digital circuits and their applications. The course offers Principles of Digital Techniques, C⁺⁺ Programming and Microcontroller at 3rd and 4th semester level. At 6th semester level the subject Industrial Electronics from digital view point has been introduced. It covers detailed study of SMPS, UPS, AC and DC drives. Signals and Systems subject covers the basis of signals and different systems. The course on Servicing Techniques is offered to give insight in servicing and maintenance aspect of the Electronics equipment.

To make the students aware of latest development in PC Architecture and Communication, subjects like Advances in Processor Architecture, Telematics and Computer Networks are introduced at VII semester level. The specialized subjects like Robotics and Embedded System Design are offered as Electives out of which a student can opt for any one. Overall the students of Digital Electronics after undergoing the course will strengthen their knowledge in the circuits, communication and applications of Digital Systems

PRACTICAL TRAINING:

The students receive institutional training for the first four semesters. In the fifth semester they are given inplant/field training. They return to the polytechnic in the 6th and 7th semesters for institutional training. In the concluding 8th semester, they again receive inplant/field training. This scheme enables the students to be exposed to industry during the training, provides for practice orientation and improves their prospects for employment.

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL.ELEX
 SEMESTER: FIRST

FULL TIME
 TEACHING & EXAMINATION SCHEMES

Sr No	Subject	Subject Code	Scheme of Instructions and Periods per week					No.of papers, duration and marks			Scheme of Examination						Gr	Scheme L/P/Cr
			L	P	D	T	Cr	NP	Hrs	Mks	SSL	Paper	T/W	PR	OR	Total		
1	Communication Skill	SS-23	3	-	-	-	3	01	03	100	20	80	-	-	-	100	B*	303
2	Mathematics-I	MA-15	3	-	-	1	4	01	03	100	20	80	-	-	-	100	B*	404
3	Physics	SC-17	4	4	-	-	8	01	03	100	20	80	25	50	-	175	B*	448
4	Applied Mechanics	AM-16	3	2	-	-	5	01	03	100	20	80	25	-	-	125	C*	325
5	Engineering Drawing-I	ME-16	2	-	6	-	8	01	03	100	20	80	50	-	50	200	C*	268
6	Workshop Technology	ME-26	-	5	-	-	5	-	-	-	-	-	50	-	50	100	C*	055
	TOTAL		15	11	06	01	33	05 Papers			100	400	150	50	100	800		

* Compulsory

L- Lecture Period
 P- Practical Period
 D-Drawing Practice Period
 NP- No.of Papers
 Mks - Marks
 SSL- Sessional
 T/W- Term Work
 Pract- Practical
 Cr- Credit

(1) Unless stated, T.W. will be assessed by the Internal examiner based on considering the continuous assessment by the concerned internal faculty

(2) Unless stated, Practical, Practical/Oral (P/O), oral will be jointly by the internal and external examiners.

(1) SUBJECT DETAILS :

Course : Diploma in Digital Electronics	Semester : I
Communication Skill	Code SS-23
Group: Basic	Compulsory

(2) TEACHING AND EXAMINATION SCHEME :

Scheme of Instructions and Periods per Week				No. of papers, duration and Marks			Scheme of Examination						Scheme L/Pr/Cr	
L	P	D	T	Cr	NP	Hrs	Mks	SSL	Th paper	T/W	Prac	Oral	Total	
3	-	-	-	3	1	3	100	20	80	-	-	-	100	303

(3) RATIONALE:

In take level of the students is S.S.C or is Equivalent. These students have studies English for minimum of 6 years. Expected level of their Active Vocabulary is 2500 words from the Nagpur list of High Frequency words. However, in practice it was found that the students, particularly from the vernacular medium have only 1500 words at their command. They need to have course in English subject to offset this situation. Therefore, the first semester engineering course students need to study English.

Further, in this age of information, Communication Skill is essential to everyone. This is equally true in the case of Engineers. Engineers having sound knowledge of the technical subjects should be able to communicate the same to his superiors, peers and subordinates in their routine work environment.

Having a sound Communication Skill will enable the engineering students to learn and interpret the subject effectively. This saves time and money and enhances the effectiveness of the engineer's performance.

(4) OBJECTIVES:

(i) The student will increase his English vocabulary to a minimum of 2500 words (Nagpur List) through the prescribed text and grammar exercises by various techniques such as synonyms, Antonyms, One word substitutes, Prefixes & Suffixes and Comprehension of new words, Idioms and Prepositional Phrases. Students will be able to use principles to solve problems in Mathematics at Higher Semester level.

(ii) Students will understand the various uses of grammar items such as Voice in English sentences, Degrees of Comparison, Tenses, Direct and Indirect Speech in formation of various kinds of sentences.

(iii) Students will master the important structures of English sentences to enhance their communication skill so that they can interact (orally and in writing) with people at different levels and in different situations with confidence.

(iv) This subject will enable the engineering students to understand the following:

- Effective Oral Communication
- Better Written Communication
- How to Improve Human Relations
- How to Overcome Barriers to Communication
- The need for Enhancing Personality

(5) DETAILED CONTENTS:

Chapter	Content	Marks	Hours
<u>SECTION – I</u>			
01	Five lessons as five units from the prescribed English Book as described in the section 'Reference' Below	20	08
02	Parts of Speech 2.1 Nouns, Pronouns & Adjectives	06	01

	2.2 Verbs & Adverbs		
	2.3 Prepositions, Conjunctions, etc.		
03	Sentences		
	3.1 Synthesis of Sentences		
	3.2 Tenses	10	02
	3.3 Degree of Comparison		
	3.4 Transformation of four kinds of sentences- Assertive, Imperative, Interrogative and Exclamatory		
04	Essay/Paragraph/Report Writing		
	4.1 Long Essay		
	4.2 Short Paragraph	16	05
	4.3 Report Writing-Visits, Accidents, Trouble, Maintenance, Memo		
05	Letters		
	5.1 Formal & Informal	16	05
	5.2 Letters for Inquiry, Order, Complaint, Application for Job		
	Total	68	21

SECTION – II

06	Dialogue Writing		
		06	01
07	Technical Description		
	7.1 Theory and Practice (Language)	08	02
08	Business Correspondence		
	8.1 Importance of Business Correspondence		

	Format of a Business Letter	24	08
	8.2 Types of Layout		
	8.3 Cardinal Qualities of Business Letter		
09	Notices and Instructions		
	9.1 Theory and Layout		
	9.2 Types of Notices	24	06
	9.3 Tender Notice (Theory and Specimen)		
10	Background of Industry		
	10.1 Definition of Terms-Industry, Industrialization, Etc.	10	02
	10.2 Factors of Production, Infrastructure		
	10.3 Industrial Estates Small Scale Industries, Trade Unions.		
	Total	(82)	(24)

(6) IMPLEMENTATION STRATEGY (PLANNING):

- (i) Five units from the prescribed text book (English for Technical Students-T.T.T.I. Chandigarh), Lessons in the prescribed text book are used as guidelines for explaining the techniques of essay/dialogue writing.
- (ii) Teaching Plan
- (iii) Grammar items are covered along with the units of lessons
- (iv) Home assignments and classroom participation

(7) REFERENCE BOOKS:

Sr.No.	Author	Title	Publishers & Address
1.	Dr. (Mrs.) Urmila Rai Dr. S.M. Rai	Business Communication	Himalaya Publishing House
2.	Edited Lessons	Communication Skill for Technical Students(National Project on Communication Skill, Chandigarh)	Somaiya Publications Pvt. Ltd., Mumbai & New Delhi
3.	Wren & Martin	Grammar & Composition	Chand Publications, New Delhi
4.	J.C.Nesfield	English Grammar, Composition & Usage	Mc.Milan, Madras
5.	Krishna Mohan Meera Banerji	Developing Communication Skill	McMilan India Ltd., New Delhi

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: I
Subject: Mathematics-I	Code:MA-15
Group : Basic	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Scheme of instruction and periods per week					No. of papers, duration and Marks			Scheme of Examination						Scheme L/Pr/Cr
L	P	D	T	Cr	NP	Hrs	Mks	SSL	Theory Paper	T/W	Pract	Oral	Total	
3	-	-	1	4	1	3	100	20	80	-	-	-	100	404

3. RATIONALE:

Algebra, Trigonometry and Coordinate Geometry are introduced as prerequisite for the topics to be studied later on to solve the engineering problems.

4. OBJECTIVES:

1. The students will be able to use the Mathematical Methods to solve the engineering problems.
2. Students will be able to use principles to solve problem in Mathematics at Higher Semester level

5. DETAILED CONTENTS:

Chapter	Content	Marks	Hours
	Section-I		
(01)	Trigonometry		
	1.1 Trigonometric ratio of any angle, Definition of Radian, Length of arc of sector $s = r\theta$,	08	02
	Area of Sector $A = \frac{1}{2}r^2\theta$	24	06
	1.2 Trigonometric ratios of allied, compound angles and multiple angles	12	04
	1.3 Inverse trigonometric functions	06	02
	1.4 Properties and solution of triangle		
	1.5 Solution of Trigonometric equation by graph		
(02)	Matrices		
	2.1 Definition of a matrix of order $m \times n$. type of matrices	02	01
	2.2 Addition and subtraction of two matrices	04	01

2.3 Multiplication of a matrix by a scalar, multiplication of two matrices.	10	03
2.4 Singular and Non-singular matrices. Transpose of a matrix. Adjoint of matrix, Inverse of a matrix A by findings Adj. A solution of simultaneous equation by using a matrix.	----	----
	(82)	(24)

SECTION-II

(03) Algebra		
3.1 Determinant	18	05
3.2 Partial fractions	16	04
	24	08
3.3 Binomial Theorem		
	16	04
3.4 Progression (A.P./G.P.)		
	08	03
3.5 Permutations and Combinations		

	(82)	(24)
Total		

(6) IMPLEMENTATION STRATEGY (PLANNING)

Conducting lectures as per the teaching plan and conducting tutorials

(7) REFERENCE BOOKS

Sr. No.	Author	Title	Publishers & Address
(i)	S.P.Deshpande	Mathematics for Polytechnic Students-I & II	Pune Vidyarthi Graha Prakashan Pune-30
(ii)	G.V. Kumbhojkar	Engineering Mathematics – I & II	Jamnadas & Co.

1.SUBJECT DETAILS :

Course : Diploma in Digital Electronics	Semester : I
Subject : PHYSICS	Code : SC – 17
Group : Basic	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							Scheme	
Theory Hrs. Per Week	Practical Hrs. Per Week		Paper			TH	SSL	T/W	Pract		Total
		Hrs	Np	Mks							
04	04	08	03	01	100	80	20	25	50	175	448

3. RATIONALE: The student has to attain a remarkable knowledge level regarding properties of materials and laws of Physics. This foundation is required at the First Year Diploma Level of various branches of Engineering and is laid by incorporating a Theory and Practical approach with due stress on practical application aspect of the subject. This is emphasized by widening scope for laboratory work, selecting such text and specialized reference books.

4. OBJECTIVES: After acquiring knowledge of Laws of Physics and co-relation of its principles, the student will:

- (i) be able to understand Application (of theoretical principles) to work
- (ii) appreciate the importance of precision involved in measurements.

5. DETAILED CONTENTS:

A. THEORY CONTENTS :

	Marks	Period
Unit: Introductory talk	-	04

To appreciate : Assertion of the statement which specifies relation between Engineering and Applied Physics.
To understand Importance of implementing precision measurements.

- Chapter: (i) Subject introduction with review of past work – `Science`, Physics, Matter and its structure, forms; `Physics` as science of measurement; unit; systems- CGS, MKS etc. multiples and submultiples
(ii) SI units and Standards- meter(m), kilogram (kg), second (s), degree Kelvin (K), candela (cd), ampere (A), mole

SECTION-I

Marks Hours

Unit- I General Physics

26 08

Chapter 1 : Elasticity

- 1.1 Elasticity, perfectly elastic, plastic, rigid body.
- 1.2 Stress, Strain, Hooke's law; Modulus of elasticity.
- 1.3 Young's modulus, Bulk modulus, Rigidity modulus; Poisson's ratio.
- 1.4 Young's modulus by Searle's apparatus, constant of elasticity. by Searle's method.
- 1.5 Elastic behavior of wire
- 1.6 Engineering applications.
- 1.7 Numerical problems.

Chapter 2: Viscosity

- 2.1 Viscosity, Streamline flow; critical Velocity.
- 2.2 Newton's Formula; Coefficient of viscosity; `poise`.
- 2.3 Poiseuille's equation, Poiseuille's Method
- 2.4 Stokes' law; Stokes; formula; Stokes method.
- 2.5 Reynold's number.
- 2.6 Applications.
- 2.7 Numerical problems

30 12

Unit-II Heat and Thermodynamics

Chapter 3: Thermometry :

3.1 Zeroth law of thermodynamics.

3.2 Platinum resistance thermometer, Standard gas thermometer,

Optical pyrometer

Chapter 4 Specific heats of gases – Cp and Cv

4.1 Specific heat at constant pressure and at constant volume-

Molar, In terms of enthalpy

4.2 Relation between Cp and Cv

4.3 Numerical problems

Chapter 5 Concepts of Thermodynamics

5.1 Heat and energy; Joule`s law; Mechanical equivalent of heat.

5.2 First law of thermodynamics

5.3 Isothermal and Adiabatic Processes

5.4 Thermodynamics processes- Reversible; Irreversible.

5.5 Cycle P-V diagram

5.6 Elementary ideas of heat engine

5.7 Concept of entropy

Chapter 6: Heat Transfer – Conduction

6.1 Heat conduction- idea of steady states; temperature gradient ; coefficient of thermal conductivity.

6.2 Thermal conductivity of a good conductor- Searle`s method

6.3 Thermal conductivity of an insulator- lee`s method.

6.4 Thermal conductivity of rubber tube by calorimetric

6.5 Temperature of interface

6.6 Numerical problems

30 10

Unit III Sound

Chapter 7 Simple Harmonic Motion (S.H.M.)

- 7.1 General equations of S.H.M.; Graphical representations;
Characteristics of S.H.M.
- 7.2 Numerical problems

Chapter 8: Waves and Oscillations

- 8.1 Progressive waves (transverse and longitudinal)-equation.
- 8.2 Principle of superposition; Stationary waves.
- 8.3 Free oscillation and forced oscillations, resonance.
- 8.4 Vibrations of air column-velocity of sound by resonance tube
closed at one end.
- 8.5 Vibrations in strings- frequency of A.C Supply by Sonometer
- 8.6 Numerical problems.

Chapter 9: Intensity of Sound

- 9.1 Loudness and phon, intensity, decibel, Logarithmic
formula
- 9.2 Data for various sources of sound and their intensity in decibel.

Chapter 10 Acoustics and reverberation:

10.1 Reverberation, Sabine`s formula Conditions for good acoustics,
Noise reduction and sound insulation Numerical problems

10.2 Ultrasonic-production and Application; Sonic, Subsonic,
Supersonic.

SECTION-II

26 06

Unit IV Optics

Chapter 11 Photoelectricity:

11.1 Photoelectric effect- Planck`s hypothesis Einstein`s equation;
Characteristics

11.2 Photocell- photo emissive; photo conductive; photovoltaic
Applications Numerical problems

Chapter 12 Elementary ideas of Interference, Diffraction and Polarization

*12.1 Interference Conditions for steady interference
pattern, Thin films, Newton`s rings, optical flatness,
Applications*

12.2 Diffraction, Grating, Applications

12.3 Polarization, Polarimeter; Photoelasticity, Applications

Unit V Electricity and Magnetism

Chapter 13 Static electricity

- 13.1 Coulomb's law; Electric field; Intensity, Electric line of force
Flux density, Numerical problems, Potential;
- 13.2 Capacitance; farad; Principle of capacitor, Capacitor; Parallel
plate capacitor; Combinations: Capacitors " In Series ", " In
Parallel; Energy of capacity; Numerical problems.

Chapter 14 Current Electricity

14.1 Specific resistance; Ohm's law as applied to complete circuit- e.m.f. and p.d.

- 14.2 Wheatstone's network and Wheatstone's bridge (W.B.) (metre
bridge) Post Office Box (P.O.B.)
- 14.3 Potentiometer
- 14.4 Application
- 14.5 Numerical Problems.

Chapter 15 Electromagnetism

- 15.1 Current carrying conductor in the magnetic field, 'ampere' the SI
Unit of current.
- 15.2 Rectangular coil carrying current placed in uniform magnetic field,
Galvanometer-Moving coil' 'Mirror', Current Sensitivity.
- 15.3 Principle of Shunt, Ammeter, Voltmeter.

Chapter 16 Magnetism

16.1 Magnetic materials; Dia, Para and Ferromagnetism; Modern

concept of magnetism.

16.2 Susceptibility, Magnetic hysteresis; Magnetic shielding.

16.3 Applications.

Chapter 17 Thermoelectricity

17.1 Thermocouple, Seebeck and Peltier effect; thermo e.m.f vs temperature relation.

17.2 Thermocouple as thermometer

30 10

Unit VI: Modern Physics:

Chapter 18 Bohr`s Theory for hydrogen atom

18.1 Bohr`s postulates

18.2 Radii of orbits and energies; Rydberg constant; Bohr model-Energy level diagram for hydrogen atom and spectral series.

Chapter 19 Crystal Structure

19.1 Crystalline and Amorphous solids, , Unit cell space-lattice cubic crystal structures- simple cubic(S.C), Body centred cubic (B.C.C), Face centred cubic (F.C.C), Atomic radius Co-ordination number, Number of atoms per unit cell; Atomic radius, Packing density..

19.2 Atomic planes and spacing, Miller indices.

Chapter 20 Elementary ideas of Lasers and Masers

20.1 Introduction related to acronym Spontaneous and stimulated (Induced) emission, Essential requirements of laser- Active medium, Resonant cavity, Pumping.

20.2 Ammonia maser, Ruby Laser; Helium –Neon Laser.

20.3 Energy and momentum of laser photon, Properties and applications.

20.4 Holography .

Chapter 21 Band theory of solids ; Semiconductors

21.1 Energy bands in solids and classification of materials in to conductors, semiconductors and insulators.

21.2, semiconductors Intrinsic ; Extrinsic – P Type and N Type; P-N junction.

Chapter 22 Elementary ideas of Fibre optics and Superconductivity

22.1 Total internal reflection ; wave guide for light, Optical fibre- Step index, Graded index ; Applications.

22.2 Principle of Superconductivity –Zero ohmic resistance; Meissner effect; Super conducting state, materials, properties and applications

172 64

Total

Scheme for PHYSICS (SC-17)

Theory introductory talk---04 hours

UNIT NO.	TOPICS	NO.OF CONTACT HOURS	MARKS	WEIHTAGE	#M/E/D
I.	General Physics	08	26	15	M
II.	Heat and Thermodynamics	12	30	18	E
III.	Sound	10	30	17	E
IV.	Optics	06	26	15	E
V.	Electricity and Magnetism	14	30	18	M
VI.	Modern Physics	10	30	17	D

Total: 64 172 100

M: Most Essential, E: Essential, D: Desirable

LIST OF EXPERIMENTS

Experiments to be performed :

Section-I (ANY 09)

1	Use of Measuring Instruments – Vernier Callipers, Micrometer Screw Gauge, Spherometer
2	Elastic Constants by Searle's Method
3	Young's Modulus by Searle's Apparatus
4	Surface Tension by Capillary Rise Method <u>OR</u> Viscosity by Poiseuille's Method <u>OR</u> Viscosity by Stokes' Method.
5	Thermal Conductivity of a Good Conductor by Searle's Apparatus
6	Ratio of Specific Heats C_p/C_v by Clement and Desorme's Apparatus
7	Thermal Conductivity of Rubber Tube by Calorimetric Method.
8	Thermal Conductivity of an Insulator by Lee's Method
9	Velocity of Sound by Resonance.
10	Velocity of Sound by C.R.O
11	Frequency of A.C. Supply by Sonometer and Verification by C.R.O.

Section – II (ANY 08)

12	Study of Spectrometer (Minimum Deviation and Refractive Index and Angle of Prism
13	Wavelength of Laser Beam (He-Ne) by Diffraction Grating.
14	Specific Rotation by Polarimeter
15	J' by Electrical Method and Specific Heat of Oil by Electrical Heating.
16	Use of Wheatstone's Bridge and Post Office Box (Resistance, Specific Resistance and Temp.Coefficient of Resistance).
17	Use of Potentiometer (Principle, Comparison of e.m.f.s of Cells, Internal Resistance of Cell and Calibration of Voltmeter with the Principle COMPULSORY and ANY ONE of the Other Three).
18	Wavelength of Light by Diffraction Grating

19	Determination of Rydberg Constant.
20	Study of Crystal Structure.

Demonstration Experiments : (ANY 07)

(i)	e.m.f. of a Thermocouple
(ii)	Spectra of Ionised Gases
(iii)	Poisson's Ratio for Rubber Tube
(iv)	Study of Photocell
(v)	Temperature of Flame (Optical Pyrometer)
(vi)	Hysteresis by C.R.O.
(vii)	Study of Newton's Rings
(viii)	Study of Para and Diamagnetism by Electromagnet
(ix)	Study of Ultrasonics
(x)	Study of Photoelastic Bench
(xi)	Optical Principles of O.H.P
(xii)	Use of Precision Measuring Instruments (Dial Vernier, Dial Micrometer, Travelling Microscope etc.)

Term-Work :

Compulsory term-work on the list of experiments written in a journal and carrying 50 marks on timely submission basis specified at the index sheet of Physics Journal.

Scheme of Practical Examination :

1	Each candidate will be examined in one experiment from among those prescribed in the syllabus. The duration of the experiment will be two hours
2	Each candidate will be asked to draw by lots any two experiments (one from section-I and the other from section-II). Out of the experiments so drawn by him/her, he/she will be asked to prefer any one experiment which he/she will be able to perform with confidence.
	Change of experiment shall be discouraged and if absolutely necessary, 05 marks will be deducted for the same after due verification of the laboratory records of the candidate.
3	The performance of the candidate in the practical examination will be assessed out of

50 Marks as shown in the following scheme:
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Scheme of assessment :

- (a) Connections, adjustment and Observations 20 Marks
- (b) Circuit diagram, figures and tabulation10 Marks
- (c) Formula, calculations and graphs if any10 Marks
- (d) Oral 5 Marks
- (e) Correct answer or appropriate result 5 Marks

Total : 50 Marks

4	Each candidate will be jointly examined by both external and internal examiners
5	Certain demonstration experiments are coupled with certain conventional experiments during the examination. Marks allotted for demonstration experiments are 15 and marks for conventional experiments are 35. The conventional experiment is assessed out of 50 as mentioned under 4 in the above scheme and then converted out of 35. Assessment of a demonstration experiment is judged by answers (Oral or Written) given by candidate in front of the relevant demonstration set up.
6	If any candidate is unable to draw a correct circuit diagram, the same may be given by the examiner on request so that the candidate may continue the experiment. However, five marks will be deducted for the same
7	While assessing, the overall performance and the ability of the candidate to handle the apparatus independently, will be considered.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Theory- Lesson Plan
2. Practical – Scheme of marking for T/W; Scheme of assessment for Pract. Exam.

7. (a) TEXT BOOKS :

Sr. No.	Author	Title	Publishers & Address
1.	R.K.Gaur and S.L. Gupta	Engineering Physics	Dhanpat Rai & Sons.
2.	M.R. Shrinivasan	Physics for Engineers	New Age International
3.	P.G. Kshirsagar and M. N. Avadhunulu	A Text Book of Engineering Physics	S. Chand and Co. Ltd
4.	Gambhir, Durgapal and Banerjee	Introductory Physics, Vols. 1, 2 and 3	Wiley Eastern

(b) REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers & Address
1.	Resnik and Halliday	Physics, Vols. 1 and 2	Wiley Eastern.
2.	B.L. Theraja.	Engineering Physics	S. Chand & Co. Ltd
3.	B.L. Theraja.	Modern Physics	S. Chand & Co. Ltd
4.	Manikpure	Applied Physics	S. Chand Co. Ltd., N.Delhi
5.	S.P. Paranjpe	Applied Physics	Central Techno Pub., Nagpur.

1. SUBJECT DETAILS:

Course : Diploma in Digital Electronics	Semester: I
Subject : Applied Mechanics	Code : AM-16
Group : C*	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs Per Wk.	Practical Hrs. Per Wk.		Paper			TH Reduced to	Sessional Marks	T/ W	Practical oral	Total
		Hrs.	N/P	Mks						
3	2	4	3	1	100	80	20	25	-	125

3. RATIONALE:

Applied Mechanics is the study of Forces and their effect on moving or stationary bodies. Also the concept of Mechanics will be very much useful to understand the further semester subjects like analysis and design.

4. OBJECTIVES:

Students will be able to:

- (1) Understand the force / force system and their effect on a body.
- (2) Apply the concept of mechanics to the real situation.
- (3) Build the pre-requisite of higher semester subject related to designs.

5. DETAILED CONTENTS:

Chapter	Topics	Periods	Marks
	SECTION-I		
1.	Introduction to Mechanics 1.1 Mechanics definition, classification, Statics & Dynamics, Kinematics, Kinetics 1.2 Fundamental units of measurement (FPS, MKS, SI), derived Units, conversion of units, Scalar & vector with examples 1.3 Definition of particle, body & rigid body, mass & weight.	02	06
2.	Resolution of Forces 2.1 Concept of Force, definition, units, graphical representation of force. 2.2 Concept of system of forces, non-coplanar, coplanar, concurrent, non-concurrent, parallel, non-parallel forces. 2.3 Resolution of forces in to two components along any directions. 2.4 Resolution of force into two components at right angles to Each other by analytical method.(Applications levers, chain & links, connected bodies like trains etc.)	04	14
3.	Composition of forces 3.1 Composition, Resultant of force 3.2 Law of parallelogram of forces 3.3 moment of force, couples lever arm 3.4 Varignon's theorem 3.5 Resultant of non-coplanar, coplanar, concurrent, non-concurrent, parallel, non-parallel forces. (Application in shafts, crane, joints of trusses etc)	04	14
4.	Equilibrium 4.1 Definition of equilibrant, relation between Resultant and Equilibrant, Condition of equilibrium, Types of Equilibrium stable & unstable and Neutral equilibrium	06	20

	4.2	Equilibrium of coplanar concurrent forces, Lami's theorem		
	4.3	Equilibrium of coplanar parallel forces & coplanar Non-concurrent forces.		
	4.4	Analytical condition of equilibrium for coplanar concurrent & non- concurrent forces (Application of crane stability, link mechanism, inclined plane, wedges, anchor blocks for water pipe lines, balance, levers, pulley & pulley blocks)		
		Practical: (a) Simple roof truss. (b) Bell crank lever. (c) Levers of different types. (d) Extension of Springs. (e) Compression of springs.		
5.		Beam Reactions	04	14
	5.1	Types of supports (constraints), simple, roller, hinged & fixed.		
	5.2	Types of beams, simply supported, hinged & roller supported, Cantilever, Over hang beams.		
	5.3	Types of loads, Point(Concentrated) load, Uniformly Distributed load (UDL), Uniformly Varying load (UVL)*		
	5.4	Problems on above combination of loads (Application like bearings of shafts, guy, rocker, roller supports of railway bridges) *no problems in UVL		
6.		Centre of gravity	04	14
	6.1	Centre of gravity of solids, centroid of plane laminas, definition		
	6.2	Concept of parallel forces applies to find c.g. and centroid centroidal / c.g. axes of a body / lamina, c.g./centroid of basic regular shapes.		
	6.3	Application like floating bodies, dams, & retaining wall sections, Beams column section (rolled steel), simple & built up sections (like floating bodies, dams, & retaining wall columns etc.)		
		Practical: (a) centroid of plane lamina		

		04	14
	SECTION-I I		
7.	Friction		
	7.1 Definition, Types of friction , static friction & dynamic friction		
	7.2 Fundamental laws of static friction .Coefficient of friction .Cone of friction. Angle of friction, Angle of repose, Rolling friction		
	7.3 Study of inclined plane, wedge & block system, ladder friction. (Application in clutches, Brakes, Dynamometer, journals, Belts & Rope drives, stator & rotor in electric motor, Brearings.)		
	Practical:		
	(a) Friction between wooden surface	04	14
	(b) Friction between wooden surface & glass, metal surfaces.		
8.	Rectilinear Motion		
	8.1 Definition of kinematics, rectilinear motion, displacement, speed, velocity, acceleration.		
	8.2 Equation of rectilinear motion with uniform acceleration.		
	8.3 Velocity- Time diagrams, motion under gravity. (Application to velocity of connecting rod, piston crank etc., V-T diagram for electric traction)	03	10
.9	Kinetics		
	9.1 Newton's law of motion, relation between force mass & acceleration		
	9.2 Application like motion of trains on slopes etc.		
	9.3 Concept of momentum & impulse, conservation of momentum. (Application centrifugal devices, motion of train on slopes etc. Power hammer, pile driving, Fly presses etc.)	04	14
10.	Work, energy & power		
	10.1 Work done by body, definition, application		
	10.2 Energy definition & types, Potential energy and Kinetic Energy, conservation of energy (Application like energy stored in fly wheel water reservoir)	04	14
11.	Simple lifting machines (Application topics)		

11.1	Definition : Mechanical advantage, velocity ratio, Efficiency Relation between them, friction in machine in terms of load & effort		
11.2	Law of Machine, Maximum M.A., Maximum efficiency Condition for reversibility of machine.		
11.3	Study of Machine-Simple & Differential axel & wheel Weston differential pulley block, Simple screw jack, Worm & Worm Wheel, Single & Double purchase crab winch, system of pulleys.	05	16
12.	Graphical Statics		
12.1	Space diagram, Bows notation		
12.2	Law of triangle of forces, Polygon of forces, Force/vector Diagram		
12.3	Resultant& equilibrium of concurrent forces		
12.4	Polar diagram , Funicular polygon		
12.5	Resultant& equilibrium of non-concurrent & non-parallel forces.	48	164
12.6	Application in finding reactions of beams, Stress in simple frame		
	Total		

6. PRACTICALS

Term work consists of Journal containing minimum 10 experiment performed of the following in the laboratory.

- 1) Extension & compression of the spring
- 2) Bell crank Lever
- 3) Two/Three sheave pulley block
- 4) Simple screw jack
- 5) Single/Three Sheave Pulley Block
- 6) Differential axel & wheel
- 7) Centroid of plane Lamina

- 8) Sheer Leg & Derrick Crane
- 9) Resultant of Non concurrent & non parallel forces
- 10) Friction
- 11) Jib Crane
- 12) Graphical Statics
- 13) Momentum of Inertia of Fly Wheel

7. REFERENCE BOOKS:

Sr .N o.	Author	Title	Publishers & Address
1.	Dhade & Jamdar	Applied Mechanics	Centro tecno publishers Nagpur
2.	R.S.Khurmi	Applied Mechanics	S.Chand & Company Ram Nagar, New Delhi-110055
3.	A.K. Tayal	Applied Mechanics	Umesh Publication
4.	Sunil M. Deo	Applied Mechanics	Nirali/Pragati Publication
5.	M. D.Dayal	Applied Mechanics	Nandu Publishers, Chembur, Mumbai-71

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester : I
Subject: Engineering Drawing – I	Code : ME-16
Group : Core	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs Per Week	Practical Hrs per Week		Paper			TH	Sessional	T/ W	Oral	Total
		Hrs	NP	Mks						
02	06	08	3	01	100	80	20	50	50	200

3. RATIONALE:

Engineering drawing is a language of engineers. It is classified as engineering science subject. It describes scientific facts, principles and technique of drawing in order to visualize and express the ideas and to convey the instructions through drawings without ambiguity. In Engineering Drawing -I, student will study concept of orthographic projections, isometric projection, isometric view, ideas of two dimensional and three dimensional objects, oblique, projections, curves etc.

4. OBJECTIVES:

Engineering drawing helps in understanding design of parts, assembly, structure etc. used in engineering field. It supports technology and technical subjects. By achieving visualization and drawing skills, the student will successfully discharge his role on shop floor, design department and inspection department etc.

5. DETAILED CONTENTS:

Chapter	Content	Marks	Hours
	<u>SECTION-I</u>		
01	1.0 Introduction :		02
	1.1 Importance of Engineering Drawing for the study of technical courses		
	1.2 Drawing instruments, types of lines and dimensioning (lettering and numbering for term work or assignment)		03
	Practice :		09
	1. A2 size sheet to be drawn containing four problems on letter lines and dimensioning techniques.		
	2. Home Assignments: One sheet containing four problems		
02	2.0 Principle, Planes and Quadrants:	-	02
	2.1 Concept of principle planes and quadrants		
	2.2 I and III angle method of projections		
03	3.0 Orthographic Views :	-	02
	3.1 Projections of various objects having flat and curved surfaces using 1 st and 3 rd angle projection method.		
	Practice:	-	12
	1. One sheet on orthographic projection for objects with linear features		
	2. Home Assignments: One sheet containing four problems		
04	4.0 Conversion of Pictorial View :	20	05
	4.1 Conversion of pictorial views in to non-sectional orthographic views. The objects may have slots, holes cavities etc.		

	Practice:	-	12
	1. One sheet with non sectional orthographic views for the objects with curvilinear features.		
	2. Home Assignments: One sheet containing four problems		
05	5.0 Oblique Projections :	30	04
	5.1 Oblique projections of curvilinear feature on non oblique plane only.		
	Practice:	-	12
	1. One sheet having four problems of oblique projection for the curvilinear features on non oblique plane.		
	2. Home Assignments: One sheet containing four problems		
06	6.0 Engineering curves :	30	04
	6.1 Cycloidal curves: Cycloid, epicycloids, hypocycloid (Simple cases)		
	6.2 Involute of circle and polygon (simple cases)		
	6.3 Helix on cylinder (not on cone)		
	Practice:	-	12
	1. One sheet with four Problems containing cycloid, epicycloids, hypocycloid , Involute and Helix.		
	2. Home Assignments: One sheet containing four problems		
SECTION-II			
07	7.0 Conversion of Pictorial View in Sectional Orthographic:	20	04
	7.1 Conversion of pictorial views with cutting plane into sectional orthographic projection (full section only)		
	Practice:		
	1. Four problems on sheet(full section plane)		
	2. Home Assignments: One sheet containing four problems		12

08	8.0 Isometric Views and Projections: 8.1 Isometric Views and Projections of linear and curvilinear features. Practice:	40	05
	1. One sheet with four problems on Isometric view having linear and curvilinear features. 2. On sheet with four problems on Isometric projection having linear and curvilinear features. 3. Home Assignments: One sheet containing four problems		12
09	9.0 Engineering Curves: 9.1 Ellipse: Oblong, arcs of circle and concentric circle method. 9.2 Parabola: Eccentricity and rectangular methods. 9.3 Hyperbola : Eccentricity and rectangular methods. Practice:	20	04
	1. Four problems on Ellipse Parabola and Hyperbola 2. Home Assignments: One sheet containing four problems		12
	Total	164	32

6. IMPLEMENTATION STRATEGY (PLANNING) :

In depth study and understanding of the subject will be implemented by adoption of the following strategy :

1. Theory Teaching Plan
2. Term Work Plan for practical giving problems to draw in the class.
3. Home assignment to practice at home
4. Conduct of three periodical test
5. Use of OHP models and charts during theory class and practical periods

7. REFERENCE BOOKS :

Sr.No.	Author	Title	Publishers & Address
1.	N.D.Bhatt and Panchal	Geometrical and Machine drawing	Rupalee Pub.Opp. Amul Dairy, Court Rd, Anand
2.	R.K. Dhawan	Engineering drawing	S.Chand & Co.Ltd., Ram Nagar New Delhi-110 055
3.	R.K. Dhawan	Machine drawing	S.Chand & Co. Ltd ,Ram Nagar New Delhi-110 055
4.	M.L. Dabhade	Engineering Graphics	Mrs.VA.Velhankar 1030, Model Colony, B-12,Akash Ganga Pune-411 016

Information to the paper setter/examiner regarding the distribution of topics section wise :

SECTION – I

Sr.No.	Topics	Periods	Marks
1	Sectional Views : <ul style="list-style-type: none">• Full section, half sectioned offset section concept.		-
2	Missing Views :		25

- Given two views (f.v.- t.v. or f.v.s.v.) deriving the third view non sectional or sectional.
3. Projection of straight lines : 10
- Projections of lines inclined to both the reference planes (no traces)
4. Projection of planes : 15
- Projection of plane – regular polygons and circle. inclined to both the reference planes.

SECTION – II

5. Projections of solids : 15
- Projection of solids like prisms, pyramids, cylinders and cones with axis inclined to both the reference planes
6. Thread profiles and screw fasteners : 15
- Different profiles of threads
 - Conventional representations of left hand – right hand threads, single and multistart square threads, external and internal threads
 - Different types of nuts
 - Different types of bolts
 - Lock nuts (Castle, slotted nut simond's nut etc. use of plane and spring washers.)
7. Keys and couplings : 20
- Detail and assembly drawing of different keys and couplings i.e. flange, oldhalm, flexible, universal etc.

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: I
Subject: Workshop Technology	Code ME-26
Group: Core (C)	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Scheme of Instructions & Periods / Week					No. of Papers, Duration & Marks			Scheme of Examination						Scheme L/Pr/Cr
L	P	D	T	Cr	NP	Hrs	Mks	SSL	Theory Paper	T/W	Practical	Oral	Total	
-	5	-	-	5	-	-	-	-	-	50	-	50	100	055

3. RATIONALE :

Production is a value adding activity, where raw material is converted into finished goods, by using different resources like man, machine, material, method, etc. Handling of different tools & equipments is a part of production system. So student should be aware of method of handling of different tools, its applications, precautions, handling procedures, etc.

4. OBJECTIVES :

- 1) The student will be able to understand workshop activities.
- 2) They will be able to select right tools and right manufacturing processes for performing the job correctly.
- 3) They will be able to use different fitting tools like Files, hacksaw, hammers, try square, chisel (cross cut chisel), center punch, etc and carpentry tools like cross cut saw, jack plate, firemen chisel, rasp file, marking gauge, mallet, scriber etc.
- 4) They will be able to read dimensions and able to do marking required for making the job.
- 5) Practical 5 periods per week (two times)

Sr.No.	Title	Hours
01.	Demonstration of carpentry tool & one carpentry joints.	25
02.	One pattern	25
03.	Demonstration of fitting tools & one job of drilling and trapping.	15
04.	One job of keyway Sawing/filing.	15
	Total	80

5. Assignment

1. Notebook containing the procedure and diagram of above jobs.
2. Notebook containing write up from workshop practices TTTI Bhopal book on above hand tools.

6. REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers & Address
1.	S.K. Hajra Choudhury, A.K. Hajra Choudhury	Elements of workshop technology (vol.I)	Media promoters and publishers Pvt. Ltd., New Delhi
2.	B.S. Rghuwanshi	A course in workshop technology (vol.I)	Dhanpatrai& sons, New Delhi
3.	W.A.J. Chapman	Workshop technology Vol-1, 2, 3	Viva books (P) Ltd.

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME

SEMESTER: SECOND TEACHING & EXAMINATION SCHEMES

Sr No	Subject	Subject Code	Scheme of Instructions and Periods per week					No.of papers, duration and marks			Scheme of Examination						Gr	Scheme L/P/Cr
			L	P	D	T	Cr	NP	Hrs	Mks	SSL	Paper	T/W	PR	OR	Total		
1	Development of Generic Skill	DG-1	2	-	-	1	3	01	03	100	20	80	-	-	-	100	B*	303
2	Mathematics-II	MA-25	3	-	-	1	4	01	03	100	20	80	-	-	-	100	B*	404
3	Chemistry	SC-27	4	4	-	-	8	01	03	100	20	80	25	50	-	175	B*	448
4	Engineering Drawing-II	ME-36	2	-	6	-	8	01	04	100	20	80	50	-	50	200	C*	268
5	Fabrication Technology	ME-46	2	4	-	-	6	01	03	100	20	80	25	-	50	175	C*	246
6	Fundamentals of Computer & N/W Systems	CA-54	2	4	-	-	6	-	-	-	20	-	50	50	-	120	C*	246
TOTAL			15	12	06	02	35	05 Papers			120	400	150	100	100	870		

* Compulsory

(1) Unless stated, T.W. will be assessed by the Internal examiner based on considering the continuous assessment by the concerned internal faculty

(2) Unless stated, Practical, Practical/Oral (P/O), oral will be jointly by the internal and external examiners.

L- Lecture Period

P- Practical Period

D-Drawing Practice Period

NP- No.of Papers

Mks - Marks

SSL- Sessional

T/W- Term Work

Pract- Practical

Cr- Credit

1. Subject Details :

Course: Diploma in Digital Electronics	Semester: II
Subject: Development of Generic Skills	Code :DG-1
Group: Basic	Compulsory

2. Teaching and Examination Scheme

Scheme of Instructions & Periods / Week					No. of Papers, Duration & Marks			Scheme of Examination						Scheme L/Pr/Cr
L	P	D	T	Cr	NP	Hrs	Mks	SSL	Theory Paper	T/W	Practical	Oral	Total	
2	-	-	1	3	1	3	100	20	80	-	-	-	100	303

3. RATIONALE

The skills of Project Management have become important in all types of business and at all levels of work hierarchy. The purpose of development of Generic Skills is to develop the necessary skills, which will make students confident and competent in managing and executing engineering projects.

4. GENERAL OBJECTIVES

Over a period of time it has been observed that effectivity of polytechnic students, their utility at work place can be enhanced by imparting generic skills right from entry in the polytechnic. The generic skills are life skills, they are lifelong skills, which needs to be sharpened every moment.

The content is divided into 4 logical units:

1. Information
2. Communication Skills
3. Self Development
4. Task Management

It is designed by keeping self in focus with a clear objective of developing Generic Skills, to enhance the capabilities in the field of searching, assimilating and using information on job. Developing self and managing given tasks, finally to present himself as a techno- manager.

It is an effort to develop student that enables him to be successful in finding a practical and reaction solution to any problem he comes across. It covers more or less all aspects of life skills. The development of subject is progressively ascending parallel to development of study in polytechnic.

5. Theory Contents:

1. Information Sources (Periods:2hrs; Mks-10)

Introduction, Types of Information sources, Print Media, Documentary Sources, Non-Documentary Sources, Non-Print Media, Electronic Media, Conclusion.

2. Information Centre (Periods:2hrs; Mks-10)

Introduction, Classification, Services, Conclusion.

3. Procedure for Information Search (Periods:4hrs; Mks-20)

Introduction, Need of Approach, Types of Approach, Steps for Information Search, Preparation of Biographic Card, Preparation of Index Card, Conclusion.

4. Learning (Periods: 4hrs; Mks-20)

Introduction, Concept of Learning, Basic Model of Learning, Principles of Learning, Conclusion.

5. Memory & Cognition (Periods: 4hrs; Mks-20)

Introduction, Basic Concepts, Dual Store Model of Memory, Sensor register Characteristics, Attention, Factors affecting attention, Figure Ground Rule, Working memory(WM); Characteristics of WM, Control Processes in WM, Long Term Memory in LTM: Characteristics of LTM, Control Processes in LTM, Organization of knowledge, Conclusion.

6. Meta Cognition & Study Strategies (Periods:4hrs; Mks-20)

Introduction, Meta Cognitive Knowledge, Self Regulated Learning, Effective Learning and Study Strategies(Covert): Selective attention, Maintain Rehearsal, Meaning learning Reflection, Internal Organization, Elaboration-----** Visualization, Effective learning strategies: Effective reading, Effective listening, Notes Taking, Conclusion.

7. Learning on Job (Periods: 4hrs; Mks-20)

Introduction, Definition, Identify General and Specific Skills, Workplace as a System, Types of System, Conclusion.

8. Learning Practical Skills (Periods: 2hrs; Mks-10)

Introduction, Process of Performing the job, Domains of Learning Job, Conclusion.

9. Testing of Acquired Skills (Periods:2hrs; Mks-10)

Introduction, Objectives, Process for Skill Analysis, Conclusion.

UNIT II COMMUNICATION SKILLS

10. Basis of Communication (Periods:2hrs; Mks-10)

Definition, Concept of Communication, Communication Cycle, Communication, Conclusion.

11. Techniques of Communication (Periods: 2hrs; Mks-10)

Introduction, Oral Communication, Written Communication, Body Language, Conclusion.

S. No.	Main Topics	No of Contact Hrs	Marks	Weightage %	#M/E/D

1	Information sources	2	10	7	E
2	Information Centre	2	10	7	E
3	Process For Information Search	4	20	11	M
4	Learning	4	20	11	M
5	Memory and Cognition	4	20	12	M
6	Meta Cognition & Study Strategies	4	20	12	D
7	Learning on Job	4	20	12	E
8	Learning Practical Skills	2	10	7	E
9	Testing of Acquired Skills	2	10	7	D
10	Basic of Communication	2	10	7	E

11	Techniques of Communication	2	14	7	E
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Total 32 164 100% #

Section I

1. Information Sources
2. Information Centre
3. Procedure for Information Search
4. Learning
5. Memory & Cognition

Section II

6. Meta Cognition and Study Strategies
7. Learning on Job
8. Learning Practical Skills
9. Testing of Acquired Skills
10. Basic of Communication
11. Techniques of Communications

Total Theory Hours : 32 Hrs.

Reference Books:

Learning to learn by Kenneth A. Kiewra
 Independent Study techniques by P.D. Kulkarni & B.B. Sharma

Additional References:

101 ways to better communication by Elizabeth Hienny.

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: II
Subject: Mathematics – II	Code: MA-25
Group: Basic (B *)	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Scheme of Instruction and Periods per week					No of papers, duration and Marks			Scheme of Examination						Scheme L/Pr.Cr.
L	P	D	T	Cr	Np	Hrs	Mks	SSL	Theory Paper	T/W	Pract	Oral	Total	
3	-	-	1	4	1	3	100	20	80	-	-	-	100	404

3. RATIONALE:

1. Creating aptitude for Mathematics of Higher Semesters
2. Focusing attention of problem solving in liberal sense. This aspect should take care of
 - (a) Intelligent combination of techniques.
 - (b) Mathematization or mathematical modeling of problems involved in various branches of knowledge.
3. Nurturing the higher order mental process of logical reasoning with rigor and precision.

4. OBJECTIVES:

These topics are introduced as

1. Prerequisite for many topics such as integral calculus, differential equations, Fourier series etc. to be studied later.
2. To introduce the applications commonly required.

5. DETAILED CONTENTS:

Chapter	Content	Marks	Hours
<i>SECTION – I</i>			
01	Co-ordinate Geometry		
	1.1 Straight line: Slope of a line; X and Y intercepts of a line, point of Intersection of two straight lines, Acute angle between intersecting lines, Perpendicular distance of a point from the line, distance between two parallel lines.	12	40
	1.2 Circle: Centre-Radius form of an equation of a circle, General equation of a circle.	10	32
	1.3 Equation of a chord, tangent and normal to any circle.	02	10
		24	82

SECTION – II

02.	Complex Number		
	2.1 Definition of a complex number	02	06
	2.2 Elementary Operations	02	06
	2.3 Polar form of a complex number, exponential form of a complex number	04	14
	2.4 Argand diagram	02	06
3.	Vector Algebra		
		01	04
	3.1 Scalars and Vectors		
	3.2 Addition of Vectors.	01	04
	3.3 Dot product and cross product	01	04
	3.4 Scalar triple product	01	04
	3.5 Application: work done by a force, moment of a force about a point.	04	08
	Area of a parallelogram Area of a triangle .Volume of the parallepiped		
04.	Function		
	4.1 Values of a function, type of functions	02	10
	4.2 Limits	04	16
		<hr/>	<hr/>
		24	82

6. IMPLEMENTATION STRATEGY (PLANNING):

Conducting lectures as per the teaching plan and conduction tutorials.

7. REFERENCE BOOKS:

Sr. No.	Author	Title	Publishers & Address
1.	S. P. Deshpande	Mathematics for Polytechnic Students – I & II	Pune Vidyarthi Graha Prakashan, Pune-30
2.	G. V. Kumbhojkar	Engineering Mathematics-I & II	Jamnadas & Co

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: II
Subject: Chemistry	Code: SC – 27
Group: Basic	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Scheme of instruction and periods per week					No. of papers, duration and marks			Scheme of examination						Scheme L/Pr/Cr
L	P	D	T	Cr	NP	Hrs	Mks	SSL	Theory Paper	T/W	Pract	Oral	Total	
4	4	-	-	8	1	3	100	20	80	25	50	-	175	4 4 8

3. RATIONALE :

This subject is classified under the category of Basic Sciences. It intends to develop the understanding of fundamental principles of chemistry and also impart knowledge of various engineering material. This will form the base for the better understanding of the other core technology and technological subjects of different branches.

4. OBJECTIVES :

After studying the subject the student will be able to

1. Understand basic principles of chemistry.
2. Apply the principles to other subject.
3. Identify different engineering materials.
4. Compare the properties of different engineering materials and select engineering materials for various purposes.
5. Apply his knowledge on day to day basis.

5. DETAILED CONTENTS:

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Atomic Structure: 1.1 Bohr's Theory of an atom 1.2 Quantum numbers 1.3 Orbits and orbitals, shape of s and p orbitals 1.4 Pauli's exclusion principle 1.5 Hund's rule 1.6 Aufbau principle 1.7 Electronic configuration of first twenty elements 1.8 Study of inert gases 1.9 Nuclear stability mass defect and binding energy 1.10 Bonding electrovalency and covalency.	16	07

Applications

Civil engineering

To understand the behavior of any material

Mechanical engineering

Material sciences , behavior of material for various applications

Electrical engineering

Fundamental of electricity like conductivity, resistivity, selection of materials

Electronics

To understand semiconductors, diodes, transistors

Plastic engineering

Polymerization , bonding

Chemical engineering

Reaction of different types, bonding ,unit operations, chemical process technology

Practice :

i) Volumetric analysis

ii) Qualitative analysis

2. **2.0 Electrochemistry :** 10 05
- 2.1 Atom, ion
 - 2.2 Arrhenius theory, degree of ionization
 - 2.3 Mechanism of electrolysis
 - 2.4 Numerical problems based on its applications industrial 2.5
 - Applications of electrolysis

Applications

Civil engineering

Corrosion control

Mechanical engineering

Electroplating of various materials, in foundry for making patterns and moulds ,material science

Electrical engineering

Electrochemical reactions , batteries, electrochemical cells

Electronics

Manufacture of PCB

Plastic engineering

Electroplating of different plastic materials

Chemical engineering

Electroplating, different electrolysis process, unit operations, chemical process technology, distillation and filtration

3. **3.0 pH –** 04 02
3.1 Concept of pH
3.2 pH scale applications of pH in industry
3.3 Examples based on hydrogen ion and hydroxyl ion concentration

Applications

Civil engineering

Concreting and quality of water

Mechanical engineering

Selection of water for cooling towers, other air-conditioning and refrigeration purposes, boilers, lube oils

Electrical engineering

Transformer oils and other instruments

Electronics

Manufacture of PCB

Plastic engineering

Reaction conditions, ABS plastic and alloys

Chemical engineering

Reaction conditions instrumentation, plant utility, chemical reaction engineering and pollution.

4. **4.0 Metallurgy :** 16 07
4.1 Types of metals & properties
4.2 General metallurgical processes
4.3 Metallurgy of iron- extraction of iron by blast furnace 4.4
Classification of steel – Based on its carbon content and its applications. Properties of cast iron, wrought iron

- and steel. Effects of elements on properties of steel
- 4.5 Heat treatment of steel – Hardening tempering, annealing and normalizing
- 4.6 Metallurgy of aluminum by Hall’s process.

Applications

Civil engineering

Engineering material

Mechanical engineering

Manufacturing of engineering tools and machine parts

Electrical engineering

Electrical engineering materials

Electronics

PCB and parts of machines

Plastic engineering

Machines and reinforced plastics

Chemical engineering

Metals for construction of plant utilities

- 5. **5.0 Alloys :** 09 02
- 5.1 Definition and purposes of alloying
- 5.2 Methods of preparation of alloys
- 5.3 Properties, composition and application of certain non ferrous alloys.
- (i) Duralumin (ii) Magnalium (iii) Monel metal
- (iv) Alnico (v) Babbit metal (vi) Gun metal
- (vii) Brass (viii) Bronze

Applications

Civil engineering

Engineering material

Mechanical engineering

Material sciences

Electrical engineering

Soldering and welding

Electronics

Thermocouple and soldering

Plastic engineering

Alloys and moulds

Chemical engineering

Petrochemicals , material of construction, chemical process technology

6. **6.0 Corrosion :** 18 07
- 6.1 Definition and types of Corrosion
 - 6.2 Mechanism of corrosion
 - 6.3 Factors affecting corrosion
 - 6.4 Methods of prevention of corrosion.
 - (i) Applying protective coatings on the surface of metal by hot dipping – galvanizing and tinning.
 - (ii) Metal cladding.
 - (iii) Electroplating.
 - (iv) Spraying.
 - (v) Sherardizing.

Applications

Civil engineering

Building repair and maintenance, concreting

Mechanical engineering

Selection of materials and prevention against corrosion

Electrical engineering

Dam conditions, maintenance of appliances, transmission of cables

Electronics

Machine parts and machine frames

Plastic engineering

Fiber reinforced plastics, bridges

Chemical engineering

Chemical process technology, pollution, material of construction

7. **7.0 Protective Coatings :** 09 02
- 7.1 Paints
 - 7.2 Characteristics of good paint
 - 7.3 Constituents of paints and their applications
 - 7.4 Varnishes
 - 7.5 Types & characteristics of varnishes
 - 7.6 Applications

Applications

Civil engineering

Engineering materials

Mechanical engineering

Material sciences

Electrical engineering

Electrical instruments & materials

Electronics

PCB making

Plastic engineering

Galvanizing , electroplating & plastics paints

Chemical engineering

Chemical process technology, pollution, material of construction

SECTION – II

- | | | | |
|----|--|----|----|
| 8. | 8.0 Water : | 22 | 09 |
| | 8.1 Impurities in natural water, physical and chemical characteristics of drinking water. | | |
| | 8.2 Hard water - Expression and estimation of hardness of water | | |
| | 8.3 Examples on the removal of hardness of water | | |
| | 8.4 Disadvantages of hard water in domestic industrial applications. Boiler feed water – Disadvantages due to hard water in the boiler | | |
| | 8.5 Methods of softening hard water. | | |
| | (i) Deionization process. | | |
| | (ii) Calgon process. | | |
| | (iii) Soda ash process. | | |
| | (iv) Permulite – Zeolite process disadvantages of hard water in domestic and industrial applications. | | |
| | 8.6 Sterilization of water – chlorination + ozonisation | | |
| | 8.7 Sewage – disadvantages & treatment | | |

Applications

Civil Engineering

Concreting

Mechanical Engineering

Hydraulics, Boilers and Engines

Electrical Engineering

Maintenance of machines, Batteries and Inverters

Electronics

Water Base Indicators

Plastic Engineering

Estimation and Manufacture of Resins

Chemical Engineering

Plant Utility, Heat Transfer Operations, Pollution and Pollution Control

Practice :

- i) Determination of hardness of water
- ii) Determination of chloride content of water

9. **9.0 Fuels :** 18 08
- 9.1 Definition and classification of fuels
 - 9.2 Calorific value of fuel units and definition
 - 9.3 Coal types and analysis of coal.
 - i) proximate ii) Ultimate analysis
 - 9.4 Characteristics of a good fuel
 - 9.5 Crude petroleum – catalytic cruding fractional distillation of crude petroleum.
Knocking of petrol in internal combustion engine and octane number.
 - 9.6 Gaseous fuel – Calorific value, composition and applications of i) water gas ii) LPG iii) Coal gas
 - iv) Producer gas v) Natural gas

Applications

Civil Engineering

Manufacturing of bricks and Preparation of Concrete mixtures

Mechanical Engineering

Thermodynamics, thermal engines, Power Engines

Electrical Engineering

Power generating stations

Electronics

Preparation of spare parts of machines

Plastic Engineering

Soldering and Welding

Chemical Engineering

Plant Utility, Petrochemicals

10. **10.0 Lubricants :** 18 08
- 10.1 Definition and types of lubricants
 - 10.2 Types of lubrication fluid film lubrication
 - Boundary lubrication, extreme pressure lubrication
 - 10.3 Properties of lubricants.
 - i) Degree of acidity ii) Saponification number iii) Viscosity iv) Viscosity index v) Flash and fire point vi) Pour point + Cloud point.
 - Working of Penskey Martin's apparatus to determine flash and fire point. Working of redwood viscometer to determine viscosity.

Applications

Civil Engineering

Selection of lubrication for civil work and small machines

Mechanical Engineering

I.C. Engines, other types of Engines, Machines

Electrical Engineering

Maintenance and working of machines

Electronics

Maintenance of machines and its parts

Plastic Engineering

Silicon release and mould release agents

Chemical Engineering

Chemical Process Technology, Plant Utility, Petrochemicals and unit operations.

10.4 Functions and Engineering applications of lubricants

Practice :

Demonstration of following experiments :

- i) Abel flashmeter
- ii) Redwood viscometer
- iii) Penskey Marteins apparatus

11. **11.0 Polymer Chemistry :** 10 04

11.1 Plastics – Definition – types of plastics

11.2 Properties of plastics

11.3 engineering application of plastics

11.4 Rubber – natural and synthetic rubber properties –
elasticity, abrasion resistance Tack industrial

11.5 Applications of rubber.

Applications

Civil Engineering

Preparation of Plumbing machines parts, taps, washers etc.

Mechanical Engineering

Manufacturing Processes

Electrical Engineering

Preparations of Electric wires and cables

Electronics

Preparations of PCB and wires of machines.

Plastic Engineering

Plastic Technology, Packaging, Co-polymer of Plastics and Additives.

Chemical Engineering

Chemical Process Technology, Petrochemicals

12. **12.0 Environmental Chemistry :** 14 03
- 12.1 Causes of pollution – air pollution types of air pollutants.
Gases – sulphur dioxide, sulphur trioxide carbon monoxide, nitrogen dioxide, carbon dioxide, control of air pollution.
- 12.2 Water pollution :
Sources of water pollution, methods of preventing water pollution.
- 12.3 Radioactive pollution
- 12.4 Noise pollution
- 12.5 Disadvantages of pollution
- 12.6 Control of Pollution

Applications

Civil Engineering

Drainage and Water Supply, Sewage Treatment

Mechanical Engineering

Thermodynamics and I.C. Engines

Electrical Engineering

Recycling of Electrical Goods

Electronics

Recycling of Electronic Goods

Plastic Engineering

Recycling of Plastics, Pollution control, Chlorofluoro Carbons and Decrease of carbons in Atmosphere

Chemical Engineering

Pollution Control and Waste Disposal, Plant Utility And Petrochemicals.

Total Number of Marks=82; Theory Hours= 64; Practical Hours= 64

6. IMPLEMENTATION STRATEGY (PLANNING):

1. **Theory topics and practice experiments should be done simultaneously. This will help the students to understand the topics.**
2. Question papers for the periodical test should cover the topics which have been taught. To test the understanding.

7. REFERENCE :

Sr.No.	Author	Title	Publishers & Address
1.	Jain and Jain	Chemistry of Engineering Materials	Dhanpat Rai Publishing Co. New Delhi
2.	Narkhede & Thatte	Engineering Chemistry	-
3.	Mahadeokar & Dr. U.P. Kodgire	Chemistry for Engineering students	Everest Publishing House, Pune
4.	B.S. Godbole	Applied Chemistry	Satya Prakashan, New Delhi

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester : II
Subject: Engineering Drawing – II	Code : ME-36
Group :Core	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme			Examination Scheme							
Theory Hrs Per Week	Practical Hrs per Week	Credits	Hrs	NP	Mks	Th	Sess	T/W	Prac	Total
02	06	08	04	01	100	80	20	50	50	200

3. RATIONALE:

Engineering drawing is a language of engineers. It is classified as engineering science subject. It describes scientific facts, principles and technique of drawing in order to visualize and express the ideas and to convey the instructions through drawings without ambiguity. In engineering drawing – II students will study interpretation of give orthographic views to find out the missing views/sectional, no-sectional or orthographic views. It also gives concept for straight lines, planes and solids in space. They are introduced to various machine drawing by screw fastness keys and couplings.

4. OBJECTIVES :

Engineering drawing helps in understanding design of parts, assembly, structure etc. used in engineering field. It supports technology and technical subjects. By achieving visualization and drawing skills, the student will successfully discharge his role on shop floor, design department and inspection department etc.

5. DETAILED CONTENTS :

Chapter	Content	Marks	Hours
	<u>SECTION-I</u>		
01	1.0: Missing Views : 1.1 Given two views (f.v. – t.v. or f.v.s.v.) deriving the third view-non sectional or sectional. 1.2 Full section 1.3 Half section 1.4 Offset section concept. Practice: 1. Two sheets with four problems each.	32	06
			24
02	2.0 Projection of Straight Lines : 2.1 Projections of lines inclined to both the reference planes (no traces) Practice: 1. One sheet with four problems 2. Home Assignments: Two sheet containing four problems	20	04
			12
03	3.0 Projection of Planes : 3.1 Projection of planes – regular polygons and circle. inclined to both the reference planes. Practice: 1. One sheet with four problems 2. Home Assignments: One sheet containing four problems	30	06
		-	12
	Total	82	64
	SECTION – II		
04	4.0 Projections of Solids : 4.1 Projection of solids like prisms, pyramids, cylinders and cones with axis inclined to both the reference planes. Practice: 1. One sheet with four problems 2. Home Assignments: One sheet containing four problems	30	05
		-	12

05	5.0 Thread Profiles and Screw Fasteners : 5.1 Different profiles of threads 5.2 Conventional representations of left hand – right hand threads, single and multistart square threads, external and internal threads. 5.3 Different types of nuts. 5.4 Different types of bolts. 5.5 Lock nuts (Castle, slotted nut, simond’s nut etc) use of plane and spring washers. Practice: 1. One sheet with Problems on various screw fastners to be sketched by free hand. 2. Home Assignments: One sheet containing four problems	20	05
06	6.0 Keys and Couplings : 6.1 Detail and assembly drawing of different keys and couplings i.e. flange, oldhalm, flexible, universal etc. Practice: 1. One sheet with problems of keys and coupling to be sketched by free hand and problem on drawing of assembly of a coupling. 2. A problem of a coupling to draw the details from the assembly. 3. Home Assignments: One sheet containing four problems	30	05
	Total	82	64

6. IMPLEMENTATION STRATEGY (PLANNING) :

In depth study and understanding of the subject will be implemented by adoption of the following strategy :

6. Theory Teaching Plan
7. Term Work Plan for practical giving problems to draw in the class.

8. Home assignment to practice at home
9. Conduct of three periodical test
10. Use of OHP models and charge during theory class and practical periods

7. REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers & Address
1.	N.D.Bhatt and Panchal	Geometrical and Machine drawing	Rupalee Pub.Opp.Amul Dairy, Court Rd, Anand
2.	R.K. Dhawan	Engineering drawing	S.Chand & Co.Ltd., Ram Nagar,New Delhi-110 055
3.	R.K. Dhawan	Machine drawing	S.Chand & Co. Ltd ,Ram Nagar,New Delhi-110 055
4.	M.L. Dabhade	Engineering Graphics	Mrs.VA.Velhankar,1030, Model Colony, B-12,Akash Ganga,Pune-411 016

Information to the paper setter/examiner regarding the distribution of topics section wise :

SECTION – I

Sr.No.	Topics	Periods	Marks
1	Sectional Views : <ul style="list-style-type: none"> • Full section, half sectioned offset section concept. 		-
2	Missing Views : <ul style="list-style-type: none"> • Given two views (f.v.- t.v. or f.v.s.v.) deriving 		25

- the third view non sectional or sectional.
- | | | |
|----|--|----|
| 3. | Projection of straight lines :
<ul style="list-style-type: none"> • Projections of lines inclined to both the reference planes (no traces) | 10 |
| 4. | Projection of planes :
Projection of plane – regular polygons and circle.
inclined to both the reference planes. | 15 |

SECTION – II

- | | | |
|----|--|----|
| 5. | Projections of solids :
Projection of solids like prisms, pyramids, cylinders and cones with axis inclined to both the reference planes | 15 |
| 6. | Thread profiles and screw fasteners :
<ul style="list-style-type: none"> • Different profiles of threads • Conventional representations of left hand – right hand threads, single and multistart square threads, external and internal threads • Different types of nuts • Different types of bolts • Lock nuts (Castle, slotted nut simond's nut etc. use of plane and spring washers.) | 15 |
| 7. | Keys and couplings :
<ul style="list-style-type: none"> • Detail and assembly drawing of different keys and couplings i.e. flange, oldhalm, flexible, universal etc. | 20 |

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: II
Subject: Fabrication Technology	Code: ME – 46
Group: Core (C*)	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credit	Examination Scheme and Maximum Marks						
Theory Hrs. per week	Practical Hrs. per week		Papers		TH	Sessional	T/W	Practical/ oral	Total
			Hrs	Paper No.	Marks	Marks	Marks	Marks	Marks
02	04	07	03	01	80	20	25	50	175

3. RATIONALE :

Fabrication technology is a subject of engineering which deals with sheet metal works. Practical applications of sheet metals, press tools terminology. Applications of press tools. Various fabrications process like welding, brazing, soldering and its practical application in day to day life.

4. OBJECTIVES :

It develops understanding of properties of sheet metal. This helps to understand working & maintenance of welding equipments. Knowledge of welding Brazing & Soldering processes & cold & hot working processes.

5. DETAILED CONTENTS :

SECTION – I

Chapters	Contents	Marks	Hours	
			Th	Pr
1.	Sheet Metal Works :	20	05	
	Ferrous & Non ferrous materials require for making sheets, sheet metal tools. Properties of sheet metal, sheet metal gauges. Sheet metal joints: types of Hems & seams.			
2.	Introduction to Press Tools :	40	06	
	Concept of shearing & punching. Principles & Methods of shearing. Definition of Blanking & Punching. Types of dies like progressive, compound & combine die. Terminology used in dies.			
3.	Mechanical cold working of Metals:	22	05	
	Terminology used for Bending & Rolling. Definition of rolling & bending. Different types of cold working processes like drawing, deep working, forming, pinning, extrusion, ring & core rolling & bending.			
Total		82	16	

SECTION – II

4.	Mechanical Hot working of Metals:	18	03	
	Meaning of Hot working, Recrystallization temperature, Different types of Hot working processes.			

5.	Welding:	18	05	
	Principle of welding, weldability, welding Representation, various welding joints. Different types of Gas welding processes & detail of Oxy-acetylene process. Different type's arc welding processes like carbon, Twin carbon, Shielded metal, Submerged, TIG, MIG, and Plasma are welding.			
6.	Brazing:	10	03	
	Definition. Material used for brazing, flux used in brazing, flux materials, Brazing process.			
7.	Soldering:	10	02	
	Definition. Material used for soldering, flux used in brazing, flux materials, soldering process.			
8.	Bolted Joints:	14	03	
	Thread terminology. Left hand & Right hand threads. Types of bolts, nuts locking arrangements for nuts.			
Total		82	16	

Teaching Plan	Use of Overhead Projector, charts & Caulk Board.
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TERM WORK :

Sr.No. JOBS

Hours

01.	Job of arc welding.	10 Hrs.
02.	Demonstration of gas welding.	10 Hrs.
03.	One job of sheet metal working including bending and shearing (Involved riveting also).	20 Hrs.
04.	One simple job involving soldering/brazing.	20 Hrs.
05.	One job of preparing nut and bolts using dies and taps.	20 Hrs.
		80 Hrs.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Lesson Planning
2. Use of Transparencies (OHP)
3. Practical jobs/demonstration
4. Use of hand tools and machine tools

7. REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers & Address
1.	S.K. Hajara Choudhury, A.K. Hajara Choudhury	Elements of workshop technology (vol.I)	Media promoters and publishers Pvt. Ltd.
2.	F.J.M. Smith	Basic Fabrication and welding Engg.	Longman Craft student series Engg.
3.	Raghuvanshi	Workshop Technology Vol-1	
4.	O.P. Khanna	Welding Technology	
5.	Donaldson	Press Tool Design	

1. Subject Details:

Course: Diploma Digital Electronics	Semester II
Subject: FUNDAMENTALS COMPUTER AND NETWORKING SYSTEM	Code: CA-14
Group: Core	Compulsory

2. Teaching and Examination Scheme

Teaching Scheme			Credits	Examination Scheme and Maximum Marks							
Theory Hrs Per Wk.	Pract Hrs per wk	Tut Hrs per wk		Paper			TH	Sessional Marks	T/W	Pract	Total
			Hrs	N/P	Mks						
2	4	-	6	-	-	-	-	20	50	50	120

3. RATIONALE:

This subject envisages to make the students know the fundamentals of computer systems and its organization. It will enable the students to comprehend the organization and working of various units of personal computer system for storing and processing information. It will also help the students to have hands on experience of operating systems and different application software used for office automation, day to day problems sharing in particular for creating business documents, data analysis graphical representations and business presentations. It also deals with basics of Internet technology available services internet connectivity and accessing information on internet. The student will also familiarize themselves with case study on Linux o.s., its design architecture, command structures, utilities and Linux.

4. OBJECTIVES:

The student will be able to:

1. Get familiarized with computerization.
2. Utilize computers in engineering /technical field.
3. Use computer concepts for Microsoft applications
4. Promote Computer Literacy and Programming Skills.
5. Made to expose towards computer area.
6. Learn networking concepts
7. Operate Internet/e-mail facility
8. Study of Linux as Operating System and Shell Programming.

5. DETAILED CONTENTS :

Chapter	Content	Marks	Hours
1	1.0 Fundamentals of Computer concepts 1.0 To acquaint with computer Hardware and Software, To get familiar with various operating system (DOS); 1.2 To use DOS internal commands ;To familiarise with DOS external commands; 1.3 To study of computer specifications PC,PC-XT, PC-AT, Pentium and its applications, 1.4 General architecture of computers; Computer peripherals (I/O Device),CD player , Storage units , floppy diskette , printers and output peripherals; 1.5 General computer terms, computer software, applications software, operating systems, utilities packages, languages, advantages of software and application packages. Practice: 1. Study of Computer hardware and peripherals	20	06
2.	2.0 Introduction to Windows:	10	05

	<p>2.1 Structure of a Window, Basic techniques for working in Windows.</p> <p>2.2 Using Menus</p> <p>2.3 Working with A dialogue box, Type of Options</p> <p>2.4 Starting Windows , Task Bar, Start Menu</p> <p>Practice: 1. Study of DOS Internal and External Commands</p> <p>2. Getting started with Windows by using different menus and working with dialogue box</p> <p>3. Study of Control panel , Screen saver and Help commands using Windows</p>		
3.	<p>3.0 Introduction to WORD package</p> <p>3.1 Starting Word; The Word Screen; Getting to Word Document:</p> <p>3.2 Typing and Editing : Editing text, Copying and Moving, Typing Special Characters (Symbols);</p> <p>3.3 Some common features: Changing the case of text, Moving & copying text with drag and drop, Justifying text.</p> <p>3.4 Creating bulleted & numbered lists, Arranging and moving between open documents.</p> <p>3.5 Finding and replacing, formatting.</p> <p>3.6 Using the spell checker, Checking grammar.</p> <p>Practice: 1. Creating, Editing and Saving a document , Table using Word package creating</p> <p>2. Document with Table ,editing using special characters & saving.</p> <p>3. Study of tool bar menus like Standard , Formatting , Tables and Borders</p> <p>4. Study of spell check , find , replace , go to , page setup , print preview and print commands.</p>	15	02
4	<p>4.0 Concepts of POWER POINT</p> <p>4.1 What is Business graphics : Types of Business Graphics, How</p>	15	02

	<p>to make an effective presentation,</p> <p>4.2 Physical aspects of presentation; A Presentation Graphics package ;</p> <p>4.3 Creating a presentation : creating a Title slide, Creating a Graph, Creating Tables, Make Organization Chart, To Save and close presentation; Working with Tools: Create , Edit, Move, Delete , Resize , Format text object, Working with Graphics tools;</p> <p>4.4 Slide show</p> <p>Practice: 1. Creating a new presentation and getting acquainted with various menus like FILE, EDIT, VIEW, INSERT, FORMAT, TOOLS, SLIDESHOW</p> <p>2 Choosing Auto Layout and working with tools and to prepare a slide show</p> <p>3. To study special effects using one slide show demonstration</p>		
5	<p>5.0 Fundamentals of EXCEL</p> <p>5.1 Starting EXCEL: What is a spreadsheet creating & editing spreadsheet, modifying the sheet.</p> <p>5.2 Study of Toolbars, Formula bar and Status bar.</p> <p>5.3 Inserting Header and footer, cells, rows, columns, worksheet.</p> <p>5.4 Formatting individual cells row, column, sheet, manipulating data by using Sort.</p> <p>5.5 Saving and Retrieving saved worksheet.</p> <p>Practice: 1.Creating Spread Sheet for various combination of computational tables.</p>	15	02
	SECTION-II		
6	<p>6.0 Linux Fundamentals</p> <p>6.1 Linux Fundamentals:- Basics of Unix and Linux.</p> <p>6.2 Multi-User and Multitasking capacities of Linux, change of password, the file types, structure of file system, important</p>	25	06

	<p>directories of the file system. Practice: 1.Linux basic commands. 2.Linux Advanced commands</p>		
7	<p>7.0 Introduction to Shell Programming : 7.1 What is Shell 7.2 Tools for working with Linux and Shell programming, Function of Shell 7.3 Access permission of file in Linux, editing files with V1, important commands related to V1 editor. 7.4 Introduction to Bash Shell Basics, Shell Bash variables, basics scripts element (input/output). 7.5 Simple Shell Programs. Practice: 1.CREATING FILE USING V1 editor, editing, saving file and quit from V1 editor. 2. Study of different run levels. 3. Shell programming-I 4. Shell programming-II. 5. Study of KDE environment</p>	24	05
8	<p>8.0 Networking and LAN commands 8.1 Network Concept and classification; 8.2 Local Area Network(LAN) : LAN Topology, LAN Software/ Operating System. 8.3 LAN commands and elementary Administrative commands like ATTACH, BROADCAST CAPTURE, LOGIN, LOGOUT, MAP, REVOKE, RIGHTS, SYSCON, SYSTEME. Practice: 1. Networking concepts and LAN commands. 2.Introduction to administrative command like Create Users, Mapping , Assigning , etc.</p>	20	02
9	<p>9.0 Introduction to INTERNET : 9.1 What is INTERNET, Application of INTERNET</p>	20	02

	<p>9.2 E-mail, TELNET, WWW, Study of various search engine using LYNX, LOGIN PROCEDURE.</p> <p>9.3 Study of INTERNET EXPLORER, Creating mailing account, Difference between SHELL and TCP/IP account.</p> <p>9.4 Surfing using WORLD WIDE WEB information relating to employment, education, alumni.</p> <p>Practice: 1. Internet terms , Use of Shell account and study of mailing , Software to send & receive mail on Hard Disk.</p> <p>2. To study internet explorer package & retrieve education related information from TCP/IP account and downloading procedure.</p>		
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Note:- Minimum of twelve experiments must be performed /completed in journal for approval of term work acceptance other than the 75% requirement of attendance.

Sr. No.	Main Topics	No. of Contact Hrs.	Marks	Weightage	
				%	# M / E / D
1.	Fundamentals of Computer concepts	06	20	12	E
2.	Introduction to Windows	05	10	07	E
3.	Introduction to WORD package	02	15	09	M
4.	Concepts of POWER POINT	02	15	09	M
5.	Fundamentals of EXCEL	02	15	09	M
6.	Linux Fundamentals	06	25	16	M
7.	Introduction to Shell Programming	05	24	14	M
8.	Networking and LAN commands	02	20	12	M

9.	Introduction to INTERNET	05	26	16	E
		-----	-----	-----	-----
	Total	32	164	100%	#

(# M=Most Essential, E=Essential, D=Desirable)

6. IMPLEMENTATION STRATEGY (Planning)

- i) Teaching Plan
- ii) Lesson Plan
- iii) Assignments

7. REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers & address
1	Hunt & Shelly.	Computer and Common sense	
2	V. Rajaraman	Computer Fundamentals	(Prentice hall)
3		PC Guide for Windows	(ITC Publication / Galgotia publication)
4	Galgotia.	Learning Word 6.0 for Windows step-by-step	
5	Preilerson	Linux: The computer reference – 5/E	
6		Unleashed Linux	
7		Mastering MS Office	BPP Publication
8	U.D. Black	Data Communications and Distributed Networks	Prentice-Hall

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME

SEMESTER: THIRD TEACHING & EXAMINATION SCHEMES

Sr No	Subject	Subject Code	Scheme of Instructions and Periods per week					No.of papers, duration and marks			Scheme of Examination						Gr	Scheme L/P/Cr		
			L	P	D	T	Cr	NP	Hrs	Mks	SSL	Paper	T/W	PR	OR	Total				
1	Mathematics-III	MA-35	3	-	-	1	4	1	3	100	20	80	-	-	-	100	C*	404		
2	C++ Programming	DE-33	3	2	-	-	5	1	3	100	20	80	25	50	-	175	A	325		
3	Electronic Workshop Technology	IE-31	2	2	-	-	4	-	-	-	20	--	50	50	-	120	C	224		
4	Electronic Devices and Circuits-I	IE-39	4	2	-	-	6	1	3	100	20	80	25	50	-	175	C*	426		
5	Electronic Materials Components & Devices	IE-32	3	2	-	-	5	1	3	100	20	80	25	-	50	175	A	325		
6	Electrical Technology	EE-38	3	2	-	-	5	1	3	100	20	80	25	50	-	175	C*	325		
7	Principles of Digital Techniques	DE-37	3	2	-	-	5	1	3	100	20	80	25	50	-	175	C*	325		
TOTAL			21	12	-	1	34	06 PAPERS			140	480	175	250	50	1095				
TOTAL PERIODS = 34							TOTAL MARKS = 1095													

* Compulsory

L- Lecture Period	SSL- Sessional
P- Practical Period	T/W- Term Work
D-Drawing Practice Period	Pract- Practical
NP- No.of Papers	Cr- Credit
Mks - Marks	

(1) Unless stated, T.W. will be assessed by the Internal examiner based on considering the continuous assessment by the concerned internal faculty

(2) Unless stated, Practical, Practical/Oral (P/O), oral will be jointly by the internal and external examiner.

1. SUBJECT DETAILS:.

Course: Diploma in Digital Electronics	Semester: III
Subject: Mathematics III	Code:MA-35
Group: C*	Compulsory

3. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hrs Per wk	Tutorial Hrs Per Wk		Paper			TH	Sessional	T/W	Pract Oral	Total
			Hrs	NP	Mks					
03	01	04	03	01	100	80	20	-	-	100

3. RATIONALE:

1. Create an aptitude for Mathematics for higher studies and creative work in Science and Technology.
2. Focusing attention of problem solving in liberal sense. This aspect should take care of :
 - a) Intelligent combination of techniques.
 - b) Mathematization or mathematical modeling of problems involved in various branches of knowledge.
3. Nurturing the higher order mental process of logical reasoning with rigor and precision.
4. Developing conceptual clarify habit of abstracting a given concrete situation and to put it in precise language and ingredients of the problems on hand.

4. OBJECTIVES:

Differential calculus is introduced as:

1. Prerequisite for many topics such as integral calculus, differential equations, fourier series etc. to be studied later.
2. To introduce the applications commonly required.

5. DETAILED CONTENTS:

Chapter	Content	Marks	Hours
	Section-I		
1	1.0 Differentiation:	57	16
	1.1 Definition		
	1.2 Derivatives by first principle		
	1.3 List of derivatives of standard functions		
	1.4 Methods of differentiation		
	1.5 Derivatives of explicit and implicit functions		
	1.6 Derivatives of parametric functions		
	1.7 Derivatives of Inverse function		
	1.8 Derivative of one function with respect to another function		
	1.9 Successive Differentiation		
	1.10 n^{th} derivative of function		
	1.11 Leibnitz theorem		
	1.12 Application of derivative	25	08
	(i) Geometric meaning of derivative		
	(ii) Equations of tangent, normal to given curve		
	(iii) Derivative as a rate measure-velocity, acceleration, related rate		
	(iv) Maxima/Minima of functions		
	(v) Radius of curvature		
	Section-II		
2	2.0 Integral Calculus:	82	24
	2.1 Integration as inverse of differentiation		
	2.2 Methods of Integration; substitution by parts, partial fractions		
	2.3 Definite integral and their properties reduction Formulae (No Proofs)		
	2.4 Application: Mean value and R.M.S value		
	Total	164	48

6. IMPLEMENTATION STRATEGY (PLANNING):

Lectures are conducted as per teaching plan and tutorials in the class.

7. REFERENCE BOOKS:

Sr.	Author	Title	Publishers & Address
1	S.P. Deshpande	Mathematics for Polytechnic Students-II	Pune Vidyarthi Griha Prakashan, Pune
2	Dr. B.S. Grewal	Higher Engineering Mathematics	Khanna Publishers 2B, Delhi
3	J.N. Wartikar P.N. Wartikar	Applied Mathematics	Pune Vidyarthi Griha Prakashan, Pune

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: III
Subject: C++ Programming	Code:DE-33
Group: A	Optional

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hrs Per wk	Practical Hrs Per wk		Paper			TH	Sessional	T/W	Pract	Total
			Hrs	NP	Mks					
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE:

C++ supports the two most popular programming paradigms procedural programming and object oriented programming. It also offers a powerful way to cope up with the real world problems. Students will be able to write better programs in C++ because it offers software's reusability, testability, portability and reliability.

4. OBJECTIVES:

After studying the subject the student will be able to:

1. List of the keywords, operators of the 'C++' language
2. Draw flowchart for programs
3. Write programs in 'C++'

5. DETAILED CONTENTS:

Chapter	Contents	Marks	Hours
	Section-I		
1	1.0 Structured programming using C++	06	04
	1.1 C++ as a superset of C programming language		
	1.2 C++ Fundamentals: Character set, Identifiers and keywords, Data types constants and Variables Declarations, Operators and expressions, Library Functions, Symbolic constants, Preprocessor Directives		
2	2.0 Data Input and Output:	06	04
	2.1 getchar(), putchar(), scanf(), printf(), puts(), cin, cout, setw(), endl etc		
	2.2 Control statements: If, If-else, Switch Loop statements: for, while, Do-while Breaking Control statements: break, continue, goto		
3	3.0 Functions And Program Structures	30	08
	3.1 Introduction		
	3.2 Defining of Function		
	3.3 Return statement		
	3.4 Types of Function		
	3.5 Actual and formal Arguments		
	3.6 Local and Global Variables		
	3.7 Default Arguments		
	3.8 Multifunction Programs		
	3.9 Storage Class Specifiers: Automatic, Register, Static, External Variables		
	3.10 Recursive Functions		
4	4.0 Pointers	16	04
	4.1 Declarations, Referencing and De-referencing, Passing Pointers to Functions, Pointers to Arrays		

	4.2 Structures and Unions: defining and Processing a structure and union		
5	5.0 Arrays	24	04
	5.1 Array notation		
	5.2 Array Declaration		
	5.3 Array Initialization		
	5.4 Processing with Array		
	5.5 Arrays and Functions		
	5.6 Multidimensional Arrays		
	5.7 Character Arrays		
	Section-II		
6	6.0 Classes and Objects	32	12
	6.1 Introduction		
	6.2 Structures and Classes		
	6.3 Declaration of Class		
	6.4 Arrays of Class Objects		
	6.5 Constructors: Copy and default constructors		
	6.6 Destructors		
	6.7 Static Class members: Static data member and static member functions		
	6.8 Friend Functions		
	6.9 Dynamic memory allocations		
	6.10 This pointer		
7	7.0 Inheritance	18	04
	7.1 Single inheritance		
	7.2 Multiple inheritance		
8	8.0 Overloading	16	04

	8.1 Function overloading: With various data types, with argument, scoping rules for function overloading, special features of function overloading		
	8.2 Operator Overloading: Overloading Assignment, Binary operator		
9	9.0 Polymorphism	16	04
	9.1 Introduction, Virtual functions, Pure virtual functions		
	Total	164	48

PRACTICE:

1. Programs on for ,do,do-while,while loop
2. Program on switch-case
3. Program on function and recursive function
4. Program on single and two dimension arrays
5. Program on pointers
6. Program on structure
7. Program on class and object, constructor, destructor, friend function
- 8 Program on single, multiple, multilevel inheritance
9. Program on function overloading and operator overloading
- 10.Program on polymorphism

6. IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Minimum 15 practicals / assignments

7. REFERENCE BOOKS:

Sr.	Author	Title	Publishers & Address
1	D. Ravichandran	Programming with C++	Tata McGraw Hill
2	Balaguruswamy	Programming in C++	Tata McGraw Hill
3	Robert Lafore	Object Oriented Programming in C++	Galgotia Publications
4	Yashwant Kanetkar	Let us C++	B.P.B. Publications

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: III
Subject: Electronics Workshop Technology	Code: IE -31
Group: C	Optional

3. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hrs Per wk	Practical Hrs Per Wk		Paper			TH	Sessional	T/W	Pract	Total
		Hrs	NP	Mks						
02	02	04	-	-	-	-	20	50	50	120

3. RATIONALE:

The printed circuit board has evolved as the main inter connection technology in modern electronics. The rise of surface mount technology has created a major change in most elements of design, technology and assembly of PCB. Soldering quality plays a major role in reliability of electronic circuit.

4. OBJECTIVES:

After studying the subject the student will be able to:

1. Do the reliable soldering
2. Identify and select proper connector for a circuit
3. Do the cable forming
4. Design and wind a transformer
5. Test coils and transformer
6. Draw an artwork for PCB
7. fabricate and test single sided PCB

5. DETAILED CONTENTS:

Chapter	Contents	Hours
1	1.0 Solder and soldering techniques: 1.1 Principles of solder connections 1.2 Solder alloys 1.3 Solder fluxes 1.4 Forming techniques 1.5 Soldering techniques 1.6 Solder mask 1.7 Reflow soldering techniques 1.8 Testing and quality control Practice 1. Assignment on different tools used in electronic workshop 2. Forming and soldering 3. Desoldering	10
2	2.0 Inter connection techniques: 2.1 BNC connection 2.2 D-type connection 2.3 FRC connector 2.4 Cable forming 2.5 Wire wrapping tool and wire wrapping 2.6 Crimping Practice 1. BNC or D-type connecting wiring 2. Cable forming	03
3	Wound components: 3.1 Different types of cores and their characteristics 3.2 Testing of coils and transformers 3.3 Manual and automatic winding machines Practice 1. transformer winding	03
4	Study of software package for a PCB design	03

5	Practice 1. design of single sided & double sided PC B for given circuit diagram 5.0 PCB fabrication: 5.1 Film master production 5.2 Properties of copper clad laminates 5.3 Board cleaning before pattern transfer 5.4 Photo printing 5.5 Screen printing 5.6 Plating 5.7 Etching 5.8 Testing 5.9 Introduction to multilayer PCB	10
6	Practice 1. One exercise of PCB fabrication for single sided or double sided PCB 6.0 Surface mount devices and technology: 6.1 Introduction to surface mount technology 6.2 Advantages of SMT 6.3 Types of SMT assemblies 6.4 SMD types 6.5 Costing of SMT equipment and assembly 6.6 Inspection methods and rework stations in SMT Practice 1. Assignment on Surface Mount technology	03
	Total	32

PRACTICE:

1. Soldering techniques.
2. Desoldering techniques.
3. Cable forming.
4. BNC connector soldering.
5. Transformer winding.
6. Testing of mains transformer.
7. P.C.B fabrication : single and double sided PCB
8. Assignment on different tools used in electronic workshop

9. Assignment on Surface Mount technology.
10. BNC or D-type connecting wiring.

6. IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Jobs on different topics

7. REFERENCE BOOKS:

Sr.	Author	Title	Publishers & Address
1	Walter C. Bosshart	Printed Circuit Board-design and Technology	TMH, New Delhi
2	NTTF Electronics Center Bangalore	PCB Design and Fabrication	NTTF Electronics Center Bangalore
3	Clyde F. Coombs Jr.	Printed circuit workbook series	McGraw Hill

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: III
Subject: Electronic Devices & Circuits-1	Code:IE-39
Group: C*	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hrs Per wk	Practical Hrs Per Wk		Paper			TH	Sessional	T/W	Pract	Total
			Hrs	NP	Mks		1			
04	02	06	03	01	100	80	20	25	50	175

3. RATIONALE :

This course enables the students to know symbol, construction, working & applications of basic electronic devices like diodes & transistor.

4. OBJECTIVES :

After studying the subject the students will be able to

1. Understand the symbol, construction & characteristics of crystal diode, zener diode & BJT.
2. Understand and explain working of different rectifier & filter circuits.
3. Understand the working & application of BJT based single stage & multistage amplifiers & their frequency response curve.
4. Analyse BJT amplifier using its h-model.
5. Design unregulated power supply & R.C. coupled amplifier.

5 DETAILED CONTENTS :

Chapter	Contents	Marks	Hours
	<u>SECTION – I</u>		
1.	1.0 Semiconductor diode characteristics : 1.1 Open circuited p.n. junction as a diode, current components in a P.N. diode 1.2 V-I. characteristics and its temperature dependence, 1.2.1 Diode resistance 1.2.2 Load line concept 1.3 Piecewise linear diode model 1.4 Transition capacitance 1.5 Diode switching time 1.6 Junction diode data sheet. Practice : 1. Study of dual channel CRO. 2. Diode (Ge, Si) characteristics and effect of temperature.	12	05
2.	2.0 Breakdown diodes : 2.1 Zener diode 2.2 Schottkey diode 2.3 V-I. characteristics 2.4 Zener diode voltage regulator 2.5 Zener and schottkey diode data sheet. Practice : 1. Zener diode characteristics and effect of temperature. 2. Study of zener voltage regulator.	08	04
3.	3.0 Rectifiers and Filters : 3.1 Half wave rectifier	32	10

	3.2 Full wave rectifiers		
	3.3 Ripple factor, T.U.F., Ratio of rectification, PIV		
	3.4 Filters :		
	L.filter, C.filter, L-C, C-L-C, Multiple L-C. and C-R-C. filter		
	their ripple factor with assumptions and voltage regulation.		
	3.5 Problems based on above topics		
	3.6 Design of unregulated power supply using L-C & C-L-C		
	filters.		
	Practice :		
	1. Study of H.W.R./F.W.R. with / without filter.		
	2. Design of unregulated power supply		
4.	4.0 Clipping and Clamping circuits :	12	05
	4.1 Series clipper		
	4.2 Shunt clipper		
	4.3 Biased clipper		
	4.4 Positive clamper		
	4.5 Negative clamper		
	Practice :		
	1. Diode clipping circuits.		
	2. Diode clamping circuits.		
5.	5.0 BJT :	18	08
	5.1 The junction transistor		
	5.2 Transistor current components		
	5.3 Transistor as an amplifier		
	5.4 Transistor configurations and characteristics		
	5.5 Graphical analysis of the C.E. configuration. Analysis of		
	cut-off and saturation regions		
	5.6 Typical transistor junction voltages		
	5.7 Transistor switching times and rating		
	5.8 Transistor as a switch.		
	Practice :		
	1. I/P and O/P characteristics of C.B. configuration.		

2. I/P and O/P characteristics of C.E. configuration.

SECTION – II

6.0 Transistor biasing and thermal stabilization :

- 6.1 The operating point
 - 6.2 Effect of temperature on operating point
 - 6.3 Bias stability 22 08
 - 6.4 Different biasing circuits and their thermal stability
 - 6.5 Bias compensation techniques
 - 6.6 Problems based on above topics.
- Practice :
- 1. BJT operating point and load lines, effect of temperature.
 - 2. To study different biasing circuits and compare their performance w.r.t temperature variations.

- 7. **7.0 Transistor at low frequencies :** 18 08
 - 7.1 Black box theory
 - 7.2 h-parameters
 - 7.3 Small signal low frequency transistor hybrid model.
 - 7.4 Analysis of C.E. configuration using accurate hybrid model
 - 7.5 Approximate h-equivalent circuit for the three transistor configurations and their comparison
 - 7.6 Analysis of common emitter amplifier with an emitter resistance
 - 7.7 Analysis of R.C. coupled multistage amplifier using h-parameter
 - 7.8 Examples based on above topics.

- Practice :
- 1. Measurement of C.E. h-parameters.
- 8. **8.0 Frequency response of BJT amplifiers :** 28 10
 - 8.1 Single stage C.E. amplifier and its frequency response

- 8.2 Effect of coupling and emitter bypass capacitors on low frequency response of an amplifier
- 8.3 Hybrid transistor model at high frequencies
- 8.4 C.E. short circuit current gain
- 8.5 Gain-bandwidth product
- 8.6 Examples based on above topics
- 8.7 Design of single stage R-C coupled C-E amplifier.

Practice :

- 1. To study the frequency response of single stage C.E. amplifier.
- 2. To study the frequency response of single stage C.B. amplifier.
- 3. Effect of CE and CC on low frequency response of an amplifier.
- 4. Design of BJT amplifier

9.

14 06

9.0 Multistage amplifiers :

- 9.1 Decibel
- 9.2 Types of coupling
 - 9.2.1. Direct coupled
 - 9.2.2. R.C. coupled
 - 9.2.3 Transformer coupled amplifiers and their frequency response
- 9.3 Effect of cascading on B.W. and gain (A_v)
- 9.4 Classification of amplifiers.

Practice :

- 1. To study the frequency response of two stage R.C. coupled amplifier.

Total

164 64

PRACTICE:

1. Study of CRO.
2. P-N junction diode characteristics.
3. Zener diode characteristics.
4. Zener diode as voltage regulator.
5. Half wave fullwave rectifier with and without filter.
6. I/P and O/P characteristics of C.B. configuration.
7. I/P and O/P characteristics of C.E. configuration.
8. Clamping and clipping circuits.
9. C-E amplifier.
10. Coupling and bypass capacitors.
- 11 Two stage R-C coupled amplifier.
12. Operating point and AC/DC load line.
13. C.B. amplifier.
14. Measurement of C.E. h-parameters.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals or assignments

8. REFERENCE BOOKS :

Sr.N o.	Author	Title	Publishers & Address
1.	Millman and Halkias	Integrated electronics	McGraw Hill International Edition
2.	Millman and Halkias.	Electronic devices and circuits	McGraw Hill
3.	Robert Boylestad	Electronic devices and circuits theory	PHI, New Delhi
4.	Allen Mottershead	Electronic devices and circuits	PHI, New Delhi

5.	Bhargava	Basic electronics and linear circuits	TTTI, Chandigarh TMH,New Delhi
6.	M.M. Shah	Design of electronic circuits & computer aided design	Wiley Easten Mumbai
7.	P.N. Thakkar, Sunil Shah	Design of basic electronic circuits	Jeevandeep Prakashan

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: III
Subject: Electronic Materials, Components & Devices	Code: IE-32
Group: A	Optional

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hrs Per wk	Practical Hrs Per Wk		Paper			TH	Sessional	T/W	Oral	Total
		Hrs	NP	Mks						
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE :

This course enables the students to know materials used for manufacturing of component & devices with their characteristics like resistive, capacitive and inductive material.

4. OBJECTIVES :

1. After studying this subject the student will be able to understand symbol, materials and their characteristics.
2. Understand the properties of different materials.
3. Understand the fabrication technique for integrated circuits.

5. DETAILED CONTENTS :

Chapter	Content	Marks	Hours
1.	1.0 Conductor / Semiconductor :	12	05
	1.1 Conductivity		
	1.2 Effect of temperature of conductivity		
	1.3 RT		
	1.4 Classification like low resistance and high resistance materials		
	1.5 Super conductivity materials, application.		

	Practice: 1. Study of Semiconductor Devices		
	2. Testing of Devices		
2.	2.0 Insulators :	12	05
	2.1 Dielectric constant		
	2.2 Dielectric strength, Dielectric loss, Dielectric polarization,		
	2.3 Various kinds of dielectric materials and their application in electronics.		
	Practice: Assignment on Insulator		
3.	3.0 Magnetic Materials :	12	05
	3.1 Electromagnetic domain theory		
	3.2 Ferro-magnetic materials, B.H. Curve, Hysteresis loop,		
	3.3 Effect of air gap on hysteresis.		
	Practice: 1. Assignment of Magnetic Material		
4	4.0 Components :	10	02
	4.1 Reliability and specification of Electronic passive and active components		
	4.2 Stability, drift, failure.		
	Practice: 1. Testing of components		
5.	5.0 Resistors :	24	04
	5.1 Brief manufacturing process		
	5.2 Rating selection criteria		
	5.3 Types of resistors and applications variable resistors.		
	Practice: 1. Study of Resistors.		
6	6.0 Cells and batteries :	12	03
	6.1 Concept of ideal voltage and current sources.		
	6.2 Types of batteries and their capacity.		
	Practice: Assignment on Cells and batteries		

SECTION-II

7.	7.0 Capacitors :	24	04
	7.1 Brief manufacturing process		
	7.2 Rating		
	7.3 Different types		
	7.4 Effect of frequency on capacitance		
	7.5 Applications.		
	Practice: 1. Study of Capacitors.		
	2.To study effect of frequencies on capacitors		
8.	8.0 Inductors :	24	06
	8.1 Types of inductors, Quality factor		
	8.2 R.F. coils, saturable inductors		
	8.3 Review of basics of transformer, Leakage inductance, core and copper loss		
	8.4 Types of transformers and applications.		
	Practice: 1. To study effect of frequencies on inductor		
9	9.0 Relays ,switches and display :	10	05
	9.1 Relay characteristics, contact rating, hold, Non pick up, Drop out, Transfer time etc.		
	9.2 Classification of relays, Rating and specifications.		
	9.3 Different types of switches like thumb wheel, Lever wheel, Rotary selector types, Matrix type switches, Sensing switches, Reed relays		
	9.4 Different types of display devices		
	Practice: 1. LED Display 2. Study of Relays & Switches.		
10	10.0 IC Fabrications :	24	09
	10.1 Monolithic IC fabrication techniques		
	10.2 Hybrid IC's (thin and thick films) Environmental testing method		
	10.3 VLSI.		
	Practice: 1. TTL Characteristics 2. Assignment on IC		
		164	48

Fabrication

Total

PRACTICE:

1. Study of Resistors.
2. Study of Capacitors.
3. Study of Diodes and transistors.
4. Testing of devices.
5. Study of relays and switches.
6. Study of LED display.
7. TTL characteristics.
8. Testing of components using curve tracer.
9. To study frequency response of capacitor.
10. To study frequency response of inductor.
11. Assignment on Cells and batteries.
12. Assignment on Insulator.

6. IMPLEMENTATION STRATEGY (PLANNING) :

- 1 Teaching Plan
- 2 Minimum 12 practicals or assignments

7. REFERENCE BOOKS:

Sr.	Author	Title	Publishers & Address
1.	Madhuri Joshi.	Electronic Material & Components	A.H.Wheeler & Co.Ltd Allahabad
2.	Prof. S.D. Raut	Electronic Material & Component Devices & Technology	Technical Publications Pune

1. Subject Details

Course: Diploma in Digital Electronics	Semester: III
Subject: Electrical Technology	Code:EE-38
Group: C*	Compulsory

2. Teaching and Examination Scheme

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory per week	Practical Per week		Paper			TH	SSL	T/W	PRACT	TOTAL
			Hrs	NP	Mks					
03	02	05	03	01	100	80	20	25	50	175

3. Rationale:

To teach the student facts, concepts, principles and procedure of operation and control of electric M/c. and applications of electrical energy in manufacturing industry which enables him to work as supervisor in a shop and as an assistant in research and development department.

4. OBJECTIVE:

Students will be able to understand

1. The concept and principle of A.C fundamentals, which can be applied to rectify electrical faults and acquire supervisory role .
2. Understand the working and construction of different types of machines, which will help them to discharge role as a supervisor in all technology areas and assist in carrying out investigation work.
3. To select the drive for a given application.

4. DETAILED CONTENTS:

Chapter	<u>SECTION-I</u> Contents	Hours	Marks	
1.	Introduction to electricity potential 1.2 Ohm's Law, resistance in series and parallel 1.3 Kirchoff's current law and voltage law Practice Kirchoff's laws- verification	2	15	1.1.1 Modern Electron Theory E.m.f. &
2.	Magnetising & Electromagnetism 2.1 Magnetic flux, magnetic flux density, magnetic field strength 2.2 Right hand Gripping rule, Maxwell's Corkscrew rule 2.3 Fleming's left hand rule 2.4 Comparison between Electric and Magnetic ckt	6	24	
3.	Electromagnetic Induction 3.1 Faraday's laws of Electromagnetic Induction 3.2 Fleming's left hand rule, Lenz's law 3.3 Dynamically induced Emf, statically induced Emf. Practice 1) Measurement of Inductance by 3 voltmeter method 2) Measurement of Inductance by 3 Ammeter method	6	20	
4.	A.C. Fundamentals 4.1 Generation of Alternating Voltage and current 4.2 RMS value, Average value, Form factor, Peak factor 4.3 A.C. through Resistances, Inductance and Capacitance. 4.4 Generation of Three phase Alternating Voltage and current 4.5 Star and Delta connections of 3 phase A.C. current Practice 1) RLC series 2) 3 phase balanced star connection, voltage relations	10	23	

3) 3 phase balanced delta connection, current relations

SECTION II

Chapter	Content	Hours	Marks
5.	Electrical Instruments 5.1 Deflecting torque, controlling torque & Damping torque 5.2 Types of Instruments 5.3 Advantages & disadvantages of PMMC & MI Instruments.	6	10
6.	D.C generator & D.C motor 6.1 Working principle of D.C generator & D.C motor 6.2 Constructional Features of D.C generator 6.3 Types of D.C generator & D.C motor 6.4 Characteristics & application of D.C motor Practice 1) Speed control of D.C generator 2) Load characteristic of self & separately excited generator 3) Magnetisation curve of D.C shunt generator	6	28
7.	Transformer 7.1 Working principles of Transformer 7.2 EMF equation of Transformer 7.3 Voltage Transformation ratio 7.4 Losses in Transformer Practice 1) Regulation of Transformer by direct loading	6	24
8.	Industrial Application of Electric Motor 8.1 General principle of single & three induction motor 8.2 Industrial Application of Electric Motor 8.3 Classification of Industrial drives 8.4 Selection Motor	6	20
Total		48	164

PRACTICE:

1. Kirchoff's laws- verification.
2. Measurement of Inductance by 3 voltmeter method.
3. Measurement of Inductance by 3 Ammeter method.
4. RLC series
5. 3 phase balanced star connection, voltage relations
- 6 3 phase balanced delta connection, current relations
7. Speed control of D.C generator
8. Load characteristic of self & separately excited generator.
9. Magnetisation curve of D.C shunt generator.
10. Regulation of Transformer by direct loading.

6. IMPLEMENTATION STRATEGY (PLANNING):

The syllabus is completed by adopting the lesson plan, by charts & by demonstration of machinery in electrical technology

7. REFERENCE BOOKS:

Sr. No	Author	Title	Publisher & Address
1.	V. K. Mehta	Basic Elect Engg	S. Chand &Company
2.	B. L. Thereja (volume I &II)	Electrical Technology	S. Chand &Company
3.	V. N. Mittal	Basic Elect. Engg	Tata Mcgraw- hill

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: III
Subject: Principal of Digital Techniques	Code: DE-37
Group: C*	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hrs Per wk	Practical Hrs Per Wk		Paper			TH	Sessional	T/W	Pract (Oral)	Total
		Hrs	NP	Mks						
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE :

Digital device technology plays a very important role in the modern world. Digital circuits are used in various day to day applications like toys, computers, calculators, satellites, microwave ovens, cellular phones etc. The digital systems with some kind of human interface will perform highly complex tasks with very high reliability and speed, unattainable by any other means. This course enables the students to learn the basic principles used in digital systems.

4. OBJECTIVES :

After studying the subject the student will be able to

1. Convert from one code to other.
2. Write the Boolean expression for a logic circuit.
3. Design combinational and sequential digital circuit.
4. Draw and analyse A/D and D/A conversion circuits.

5. DETAILED CONTENTS :

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Number system and codes : 1.1 Binary 1.2 Octal 1.3 Hexadecimal 1.4 BCD 1.3 Gray Practice: 1. Assignment on code conversion	06	02
2.	2.0 Basic logic gates, Universal gates : 2.1 Boolean algebra 2.2 DeMorgan's Theorems Practice: 1. Verification of DeMorgan's Theorems. 2. NAND/NOR as universal gates.	08	03
3.	3.0 Arithmetic elements : 3.1 Half adder, Full adder 3.2 Half subtractor, Full subtractor 3.3 1's complement, 2's complement subtraction 3.4 BCD adder Practice: 1. Half adder and Full adder using basic gates	08	03
4.	4.0 Design of combinational circuits : 4.1 K Map 4.2 SOP and POS forms of equations 4.3 Min terms, Max terms 4.4 Design of circuits using universal gates Practice : 1. Design of combinational circuit	30	08

5.	5.0 Study of MSI, LSI circuits : 5.1 Decoders, Encoders 5.2 Multiplexers, Demultiplexer 5.3 4 bit parallel adder 5.4 4 bit comparator 5.5 Parity generator, checker 5.6 Arithmetic and logic unit 5.7 PLA, PLD Practice: 1. Study of decoder ICs. 2. Study of Multiplexer ICs. 3. Study of 4 bit parallel adder . 4. Study of 4 bit comparator . 5. Study of parity generator/checker. 6. Study of ALU	30	08
SECTION-II			
6.	6.0 Sequential circuits : 6.1 Difference between combinational and sequential circuits. 6.2 Triggering of sequential circuits. 6.3 Flip flops – R-S, J-K-T, and D types. 6.4 Counters – Synchronous and asynchronous basic concept and design using excitation tables of flip flops e.g. Binary, BCD, Up/Down, Pre settable counters. 6.5 Four digit up/down counter IC 7217. 6.6 Shift registers – serial and parallel shift, universal shift register, and ring counter. Practice: 1. J-K flip flop (7476), D flip flop (7474). 2. Design on counter using flip flop & gates. 3. Study of counters ICs. 4. Study of shift register ICs.	32	09
	7.0 Semi-conductor memories : 7.1 Random Access Memory : Static memory cell, 7.2 Dynamic memory cell, Internal organization, Read/Write operation.	20	06

7.3 Read Only Memory: Erasable Programmable ROM, EEPROM.		
7.4 Memory Expansion – Serial and parallel expansion.		
7.5 Study of memory ICs.		
Practice:		
1. Study of memory ICs.		
2. Serial and parallel expansion of memory		
3. Assignment on memory IC's.		
8. 8.0 Logic families :	10	03
8.1 Comparative study of different logic families like TTL, CMOS, ECL etc. considering following characteristics: Logic levels, Power dissipation, Fan in / Fan out, Noise immunity, Speed of operation.		
8.2 Interfacing of ICs of different logic families.		
Practice: 1. Interfacing of TTL & CMOS ICs		
9. 9.0 Study of A/D & D/A Converters:	20	06
9.1 Study of Analog to Digital conversion techniques successive approximation A/D converter – Dual slope A/D conversion, Flash A/D conversion, ICs based on each conversion method.		
9.2 Study of Digital to Analog conversion Binary weighted resistor method, R-2R, Ladder method, ICs based on D/A conversion technique.		
Practice:		
1. Study of D to A conversion.		
2. Study of A/D conversion.		
Total	164	48

PRACTICE:

1. Study of logic gates.
2. Verification of De-Morgan's theorem.
3. NAND as a universal gate.
4. NOR as a universal gate.
5. Study of half and full adders.
6. Study of decoder.
7. Study of BCD to seven segment decoder/driver.
8. Study of multiplexer.
9. Designing of combinational circuit.

10. Study of JK flipflop.
11. Study of D type flip flop
12. Study of decade counter.
13. Study of four bit parallel adder.
14. Study of DAC.
15. Study of ADC.
16. 4-bit comparator.
17. Parity Generator and Checker.
18. Study of ALU.
19. Shift Register.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals/assignments.

7. REFERENCE BOOKS:

Sr.No	Author	Title	Publishers & Address
1.	Albert Paul Malvino & Donald P. Leach	Digital Principles & Application	Mc-Graw Hill International
2.	Morris Mano	Digital Logic & Computer Design	PHI, New Delhi
3.	James Bignell & Robert Donovan	Digital Electronics	Delmar & Thomson Learning
4.	TTL Data Sheets	-	-
5.	Intersil Data Sheets	-	-
6.	National Semi-conductor Data Sheets	-	-
7.	R.P. Jain.	Modern Digital Electronics.	TMH

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME
SEMESTER: FOURTH TEACHING & EXAMINATION SCHEMES

Sr No	Subject	Subject Code	Scheme of Instructions and Periods per week					No.of papers, duration and marks			Scheme of Examination						Gr	Scheme L/P/Cr	
			L	P	D	T	Cr	NP	Hrs	Mks	SSL	Paper	T/W	PR	OR	Total			
1	Advanced Mathematics	MA-45	3	-	-	1	4	1	3	100	20	80	-	-	-	100	C	404	
2	Microprocessor & Microcontroller	DE-41	4	2	-	-	6	1	3	100	20	80	25	50	-	175	A*	426	
3	Circuits & Networks	IE-47	4	2	-	-	6	1	3	100	20	80	25	-	50	175	C	426	
4	Principles of Analog Techniques	IE-48	3	2	-	-	5	1	3	100	20	80	25	50	-	175	C*	325	
5	Measurement Techniques & Measuring Instruments	IE-41	3	2	-	-	5	1	3	100	20	80	25	-	50	175	C*	325	
6	Electronic Devices & Circuits-II	IE-40	4	2	-	-	6	1	3	100	20	80	25	50	-	175	C*	426	
7	Industrial Management-I	ME-01	3	-	-	-	3	1	3	100	20	80	-	-	-	100	M*	303	
TOTAL			24	10	-	1	35	07 PAPERS			140	560	125	150	100	1075			
			TOTAL PERIODS = 35						TOTAL MARKS = 1075										

* Compulsory

L- Lecture Period SSL- Sessional
P- Practical Period T/W- Term Work
D-Drawing Practice Period Pract- Practical
NP- No.of Papers Cr- Credit
Mks - Marks

- (1) Unless stated, T.W. will be assessed by the Internal examiner based on considering the continuous assessment by the concerned internal faculty
- (2) Unless stated, Practical, Practical/Oral (P/O), oral will be assessed jointly by the internal and external examiners.

1.SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: IV
Subject: Advanced Mathematics	Code:MA-45
Group: C	Optional

2.TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Th Hours per week	Tutorials Hrs. Per Wk.		Paper			TH	Sessional	T/W	Pract. Oral	Total
			Hrs	N P	Mks					
03	01	04	03	01	100	80	20	-	-	100

3.RATIONALE:

Advanced Mathematics is classified as Basic Science Subject which intends to teach students the facts concepts and principles of Mathematics those which can be applied to solve problems in Electrical.

4.OBJECTIVES:

The student will be able to evaluate integrals with the help of definition, formulae, methods and theorems.

The student will evaluate problems with the help of definitions and properties of definite integrals and reduction formulae.

The student will be able to represent complex numbers as Argand's diagram. Using algebra of vectors, student will be able to find out the work done by a force, moment of a force about a point, area of a triangle, volume of Parallelopiped.

Student will solve simultaneous equations by using matrices.

5.DETAILED CONTENTS:

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Applications of definite integrals 1.1Length of arc 1.2Areas 1.3Volumes 1.4Centre of Gravity 1.5Moment of Inertia	42	16
2.	2.0Complex numbers 2.1De Moivre's theorem 2.2Roots of a complex number 2.3Circular and Hyperbolic functions of a complex numbers relation between them. 2.4Separation of real and imaginary part of a complex number.	40	16

SECTION-II

3.	3.0Laplace Transformations 3.1Introduction, Definitions 3.2Laplace Transforms of elementary functions Introduction, Definitions 3.3Laplace Transforms of elementary functions $1, t^n, e^{at}, \sin(at), \cos(at)$ 3.4Properties of Laplace Transform 3.5Inverse Laplace Transform 3.6Transform of derivatives & integrals	22	12
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4.	4.0Differential Equations	60	20
	4.1Formation of differential equation		
	4.2Differential equations of first order and first degree		
	4.3Linear differential equations of higher order with constant coefficients such as $f(D)y = X$, where $X = e^{ax}$, $\sin(ax + b)$, $\cos(ax + b)$, x^m , $e^{ax}V$, where V is a function of x .		
	4.4Applications of differential equations.	164	64

Total

6. IMPLEMENTATION STRATEGY (PLANNING):

Lectures are conducted as per teaching plan and tutorials in the class.

7. REFERENCE BOOKS:

Sr.No.	Author	Title	Publishers & Address
1.	S.P. Deshpande	Calculus for Polytechnics	Pune Vidyarthi Griha Prakashan, Pune
2.	Dr. B.S. Grewal	Higher Engineering Mathematics	Khanna Publishers 2B, Delhi
3.	J.N. Wartikar P.N. Wartikar	Applied Mathematics	Pune Vidyarthi Griha Prakashan, Pune

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: IV
Subject: Microprocessor & Microcontroller	Code:DE-41
Group: A	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs per week	Practical Hrs per week		Paper			Th	SSI	T/W	Practical	Total
			Hrs	N P	Mks					
04	02	06	03	01	100	80	20	25	50	175

3. RATIONALE:

Microprocessors & microcontrollers are now integral part of an industry. It is used as a tool to control a large process. The general purpose microprocessor like 8085 and single chip microcontroller like 8051 are the best example at the introductory level. Thus with a mix of general processing techniques and application orientation, this course stands as a sound fundamental support for advance processors as well as process control system.

4. OBJECTIVES:

1. After studying this subject student will be able to
2. Draw and explain architecture of both 8085 & 8051
3. Write basic as well as some application programs on both
4. Design application 8051 microcontrollers
5. Interface different hardware devices to both microprocessors & microcontrollers

6. DETAILED CONTENTS:

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Study of Intel 8085 Microprocessor 1.1 Architecture 1.2 Pin Functions	16	06
2.	2.0 Study of single chip microcontroller 2.1 Comparison of μ P & μ C 2.2 Architecture 2.3 Pin functions Timing diagram 2.4 Port Structure 2.5 Interrupt System 2.6 UART 2.7 Timer/Counter 2.8 Addressing Modes 2.9 Instruction set & Programming in Assembly language & C Practice: 1. Programs using 8051 Assembler & Cross C Compiler	50	20
3.	3.0 Study of Software 3.1 Editor ,Assembler, Linker, Simulator 3.2 Cross C Compiler	16	06

SECTION-II

4	4.0 Study of Interfacing IC's 4.1 Programmable Peripheral Interface 4.2 Programmable Keyboard /Display Controller	32	12
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Practice :

1. Interfacing Programs with Programmable Peripheral Interface
2. Interfacing Programs with Keyboard/Display Controller

5	5.0 Minimal System Design using Microcontroller 5.1 Memory 5.2 Keyboard ,Display 5.3 A\D converter 5.4 Peripheral Control Practice: 1Design Assignment 2. Programs Based on Interface	24	04
6	6.0 Study of Applications like 6.1 Programmable Sequential Timer 6.2 Hardware Digital Clock 6.3 Temperature Controller 6.4 Elevator Controller 6.4 Stepper Motor Interface Practice: Applications like 1. Programmable Sequential Timer 2. Hardware Digital Clock 3. Temperature Controller 4. Elevator Controller	18	10
7.	Derivatives of 8-bit microcontroller Features & comparison	08	06

Total 164 64

Practice:

1. Programs using 8051 Assembler & Cross C Compiler.
 - a. Basic programs.
 - b. Program on timer/ counter/serial port.
 - c. Program on interrupt.
2. Interfacing Programs with Programmable Peripheral Interface
3. Interfacing Programs with Keyboard/Display Controller.
4. Design Assignment (minimal system design)
5. Programs Based on Interface
6. Study of following applications:
 - a. Programmable Sequential Timer
 - b. Hardware Digital Clock
 - c. Temperature Controller
 - d. Elevator Controller

6.IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Minimum 12 practical or assignments.

7. REFERENCE BOOKS:

Sr.No.	Author	Title	Publishers & Address
1.	Ramesh S. Gaonkar	Microprocessor, Architecture Programming & Applications with 8085	Penram International Pub. House
2.	Kenneth J. Ayala	The 8051 Microcontroller, Architecture Programming & Applications	Penram International Pub. House
3.	Intel Data Sheets		
4.	Ali Mazidi M. and J.G. Mazidi	The 8051 Microcontroller and Embedded Systems	Pearson Education

Web Reference:

www.intel.com

www.atmel.com

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics

Semester: IV

Subject: Circuits & Networks

Code:IE-47

Group: C

Optional

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. Per Week	Practical Hrs. Per Week		Paper			TH	SSL	T/W	/Oral	Total
		Hrs	NP	Mks						
04	02	06	03	01	100	80	20	25	50	175

3. RATIONALE:

This first course in circuit analysis and synthesis lays down foundations for many vital concepts which will be developed in the subsequent courses.

4. OBJECTIVES:

After studying the subject the student will be able to

1. Analyze AC/DC circuits using basic analysis techniques.
2. Synthesis of circuits using parameters.
3. Find transient response of first order RC & RL ckts.
4. Understand concepts of mutual/coupled circuits.

5. DETAILED CONTENTS :

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Network analysis and theorems : Loop and Nodal analysis of linear ckts., Superposition, Norton, Millman and Maximum power transfer theorems, Their practical & theoretical proofs. (wherever applicable). Practice: 1.Thevenin's theorem. 2.Norton's theorem. 3.Superposition theorem	30	12
2.	2.0 A.C. Circuits : 2.1 Frequency response and approximate plots (Bode's plots) of following ckts – Compensated attenuator, R.C. low pass and high pass filters, lead and lag networks. The transfer functions of these circuits should also be derived. 2.2 Series R-L-C and parallel R-L-C ckt. Series and parallel resonance. Derivations for frequency if resonance. Selectivity, Bandwidth and Q pt. Concept. Practice: 1.Series resonance 2.Parallel resonance 3.Lead-lag circuits 4.R-C filters	18	06
3.	3.0 Two Port Networks : Z,Y,H parameters and their conversion into each other. Practice: 1.Evaluation of Z,Y,H parameters for simple circuits	18	08
		16	06

SECTION-II

4.	4.0 Transient Response : 4.1 Periodic switching of R-L and R-C ckts., Rise and fall times, charging and discharging process 4.2 Derivation of equation for voltage across capacitor & inductor when switched on to a DC/AC supply 4.3 Differentiator and Integrator ckts. 4.4 Solutions of linear differential equations by classical method and its implication in transient response Practice: 1.Time constant of R-C circuits 2.Differentiator and Integrator	32	12
5.	5.0 Transmission Lines : 5.1 General equations of transmission lines 5.2 Expression for primary and secondary constants of a line 5.3 Open circuited line, characteristics impedance, 5.4 Loading of a line, matched conditions 5.5 Reflection & reflection co-efficient. Practice: 1.Study of attenuator 2.Study of transmission line	17	06
6.	6.0 Coupled circuit : 6.1 Mutual self and driving pt. impedance 6.2 Transfer impedance 6.3 Inductive and capacitive coupling 6.4 Transformer impedance matching equivalent circuit Practice: 1. Assignment of couple circuits	17	06
7	7.1 FILTER THEORY: LC FILTERS,Constant K & M derived type filters, half section, design of low pass, High pass, Band pass & Band reject filters using constant K & M derived filter structure (I & II type) PRACTICE:	16	08

LC FILTER assignment

TOTAL

164

64

PRACTICE:

1. Study of Thevenin's theorem.
2. Study of Norton's theorem.
3. Study of Superposition theorem.
4. Study of Maximum power transfer theorem
5. Study of R-C high pass and low-pass filters.
6. Study of Y parameters.
7. Study of Z parameters.
8. Study of Integrating circuits.
9. Study of Differentiating circuit.
- 10.. Study of Series R-L-C circuits.
11. LC filter assignment.
12. Assignment on coupled circuits.
13. Study of attenuator .
14. Study of transmission line.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals/assignments in the term

7. REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers & Address
1.	Philip Cutler	Electronic ckt. Analysis – Vol.1	McGraw Hill
2.	Kaur et al	Networks, Transmission lines & filters	PHI, New Delhi
3.	Sudhakar Shyamsunder	Circuits & Networks	TMH

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: IV
Subject: Principles Of Analog Techniques	Code:IE-48
Group: C*	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. Per Week	Practical Hrs. Per Week		Paper			TH	Sessional marks	T/W	Pract	Total
			Hr s.	N/ p	Mk s.					
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE :

Analog circuits play a vital role in the field of electronics. This course lays down the concepts of feedback amplifiers (Positive and Negative), timers, sweep generator etc. which are used in many electronic applications.

4. OBJECTIVES :

After studying the subject the student will be able to

1. Draw and analyze the circuits of Operational amplifier, Oscillators, Feedback amplifier, Timers.
2. Identify the need for voltage and current time base generators, blocking oscillators, voltage multipliers.
3. Measure Opamp parameters.

5. DETAILED CONTENTS :

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Feedback Amplifier (Negative feedback) :	10	04
	1.1 Feedback concept and classification		

	1.2 Effect of negative feedback on		
	1 Bandwidth		
	2 Voltage gain		
	3 Noise		
	4 Input/Output impedance		
	5 Distortion		
	6 Stability		
	1.3 Feedback circuit using transistor		
	1 Voltage series		
	2 Voltage shunt		
	3 Current series		
	4 Current shunt		
	Practice: 1. Assignment on feed back		
	2. Study of Voltage shunt feedback		
2.	2.0 Oscillators (Positive Feedback) :	30	08
	2.1 Condition for maintaining oscillations		
	2.2 Derivation of frequency of oscillations for the following oscillators using transistors		
	1 RC phase shift oscillator		
	2 Wein bridge oscillator		
	3 Tuned collector oscillator		
	4 Hartley / Colpitts oscillator		
	5 Crystal oscillator		
	Practice:		
	1. Wein bridge oscillator		
	2. RC phase shift oscillator		
	3. Crystal oscillator		
3.	3.0 Operational Amplifier :	42	12
	3.1 Block diagram of Opamp, Differential amplifier, Level shifting stage, Darlington amplifier and overload protection		

circuit.

3.2 Operational amplifier parameters.

3.3 Characteristics of Opamp.

3.4 Opamp applications.

1 Inverting / Non-inverting amplifier

2 Adder / Subtractor

3 Voltage follower

4 Integrator, Schmitt trigger

5 Voltage limiters, Dead zone circuits

6 Log / Antilog amplifier

7 Precision rectifiers (half wave, full wave)

8 Active filter (first order and concept of higher order filters)

9 Wein bridge oscillator

10 Square wave generator

Practice:

1 Opamp applications

1. Inverting / Non-inverting amplifier using Opamp.

2. Integrator.

3. Differentiator

4. Precision rectifier.

5. Log / Antilog amplifier.

6. Dead zone circuit.

7. Comparator.

8. Schmitt trigger.

9. Filter (low pass, high pass)

10. Adder/Subtractor

SECTION-II

	4.1 Internal block diagram		
	4.2 Monostable, Astable, Bistable multivibrators		
	4.3 Typical applications		
	Practice: Timer circuits using 555 timer IC		
	1. Astable multivibrator		
	2. Mono stable multivibrator		
5.	5.0 Voltage and current sweep generators :	21	08
	5.1 General features of a time base signal.		
	5.2 Voltage and current sweep generator circuits.		
	1 Exponential sweep.		
	2 UJT sweep circuit.		
	3 Miller's theorem and Miller time base generator.		
	4 Bootstrap time base circuit		
	5 Transistor constant current sweep		
	Practice: 1. To study UJT Sweep circuit		
6.	6.0 Blocking Oscillator :	05	02
	6.1 Transformer blocking oscillator (Base and emitter timing).		
	6.2 Astable blocking oscillators (diode & RC controlled).		
	Practice:1.Assignment on Blocking Oscillator		
7.	7.0 Voltage Multipliers :	10	02
	Voltage doublers, tripler & quadrupler, applications.		
	Practice: 1.Voltage doubler circuit		
8.	8.0 Analog Integrated Circuits :	16	04
	8.1 Universal active filter		
	8.2 Switched capacitor butter worth filter		
	Practice:1. To study active filter IC		

Total

PRACTICE:

1. Inverting / non-inverting amplifier using opamp.
2. Op-amp as adder and scalar.
3. Study of op-amp parameters.
4. Op amp as differentiator.
5. Op amp as integrator.
6. Hysterisis comparator using op- amp.
7. Square wave generator using op amp.
8. Transfer characteristics of inverting amplifier.
9. Astable multivibrator using 555 timer IC.
10. Transfer characteristics of a dead zone circuit.
11. Gate crystal oscillator.
12. Schmitt gate oscillator
13. RC phase shift oscillator.
14. Voltage shunt feedback.
15. Voltage doubler.
16. Study of comparator.
17. Precision rectifier.
18. Active filter.
19. To study UJT sweep circuit.
20. Wein bridge oscillator.
21. Log / Antilog amplifier.
22. Monostable multi vibrator using IC 555.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan

2. Minimum 12 practicals / Assignments

7. REFERENCE BOOKS :

Sr.No.	Author	Title	Publishers & Address
1.	Allen Mottershead	Electronic Devices & Circuits	PHI, New Delhi
2.	Jacob Millman Herbert Taub	Pulse, digital and switching waveforms	TMH, New Delhi
3.	Ramakant Gaikwad	Linear integrated circuits and Opamp	PHI, New Delhi
4.	J.Machael Jakob	Applications and Design with Analog integrated circuits	PHI, New Delhi

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: IV
Subject: Measurement Techniques and Measuring Instruments	Code:IE-41
Group: C *	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. Per Week	Practical Hrs. Per Week		Paper			TH	Sessional marks	T/W	Oral	Total
			Hrs.	N/p	Mks.					
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE :

To become a good Technician in electronics, the student must understand, select, measure, and use proper testing & measuring instruments and must be aware of all errors & remedies. They should also be capable of measuring all parameters and use methods in connection with test and measuring procedure.

4. OBJECTIVES :

After studying the subject the student will be able to

1. Identify, label the PMMC instrument.
2. Draw circuits of electronic voltmeter, FET voltmeter.
3. Measure voltage & current and extend the range of measuring instruments.
4. Measure resistance, inductance, capacitance using DC/AC bridges and using ohmmeters.
5. Measure voltage, current, frequency & phase using CRO.
6. Draw signal generator AF/RF diagrams and use it for testing purpose.
7. Measure Q of a circuit and dissipation factor using LCR-Q meter.
8. Measure distortion in an amplifier.

9. Use D.S.O. for measuring applications.

5. DETAILED CONTENTS :

		<u>SECTION-I</u>	
Chapter	Content	Marks	Hours
1.	1.0 Measurement Terminology: 1.1 Definition of measurement & instruments, 1.2 Accuracy, error, precision, range, sensitivity 1.3 Types of errors in measurement and remedial methods 1.4 Merits & de-merits Practice: 1. Assignment on Measurement terminology	16	03
2.	2.0 Standards: 2.1 Requirements of standards 2.2 Primary, secondary, working standards 2.3 Concept of calibration & calibration check uncertainty. Practice: 1. Assignment on need of standards and Calibration	10	03
3.	3.0 Permanent magnet moving coil galvanometer: 3.1.Theory, working & construction of PMMCG 3.2 Measurement of voltage current and resistance using PMMCG 3.3 Extension of range, Ayrton shunt 3.4 PMMCG multi meter advantages and limitations Practice: 1. Extension of range of ammeter 2. Extension of range of volt meter 3. Series ohmmeter 4. Shunt ohmmeter	20	06
4.	4.0 Electronic volt/Ohmmeters : 4.1 Principle, working of FET voltmeter	28	08

	4.2 Transistorized voltmeter		
	4.3 AC milli volt meter		
	4.4 Chopper type electronic DC volt meter.		
	4.5 Ohmic sensitivity of volt meter		
	4.6 Loading effect, remedies, concept of high input impedance to measuring instrument		
	4.7 Principle and operation of DVM		
	4.8 DVM specifications		
	4.9 Measurement of AC/DC voltage, current and resistance		
	4.10 Advantages of DVM, types of DVM.		
	Practice: 1. FET Volt meter		
	2. Loading effect measurement		
	3. Rectifier type instrument		
5.	5.0 Time and frequency Measurement:	08	04
	5.1 Frequency and time standards		
	5.2 Measurement of frequency		
	5.3 Block schematic of frequency counter		
	5.3 Measurement of frequency and time		
	Practice: 1. Assignment on frequency counter		
	2. Assignment on voltage to time conversion		
	<u>SECTION-II</u>		
6.	6.0 Resistance and impedance bridges :	20	06
	6.1 Introduction to DC resistance bridges		
	6.2 Errors in balancing bridge		
	6.3 Kelvin double bridge		
	6.4 Concept of guard applications		
	6.5 Potentiometric measurements		
	6.6 AC bridges Types of sources and detectors		
	6.7 Maxwell's bridge		
	6.8 Hay's bridge		

	6.9Capacitance comparison bridge and RLC bridge, applications		
	Practice : 1. Demonstration of RLC bridge		
	2. Assignment on AC bridges to calculate unknown values of L,C		
	3. Measurement of input impedance		
7.	7.0 Signal generators :	16	04
	7.1 Block schematic of A.F. and R.F. generators, explanation of each block inside		
	7.2 Specification and application of signal generators		
	7.3 Function generator		
	7.4 Block diagram		
	Practice: 1.Assignment on schematic diagram of AF signal generator along with the power supply and attenuator		
	2.Demonstration on RF Generator		
8.	8.0 Q Meter :	16	03
	8.1 Theory and explanation of the circuit		
	8.2 Measurement of Q,L,C and dissipation factor.		
	Practice: 1. Measurement of Q & dissipation factor of C		
9.	9.0 Distortion factor meter :	16	04
	9.1 Harmonic distortion		
	9.2Theory of tuned harmonic analyzer		
	9.3Heterodyne harmonic analyzer		
	9.4Measurement of harmonic distortion		
	Practice: 1. Measurement of distortion		
10.	10.0 Cathode Ray oscilloscope:	14	07
	10.1 Study of block diagram and function of each block of CRO		
	10.2 Deflection sensitivity and Deflection factor of CRT		
	10.3 Special features of dual trace, double beam, delayed sweep and storage oscilloscopes		

- 10.4 Measurement of voltage, frequency, phase
- 10.5 Study of Lissajous, Z-modulation, waveform analysis and comparison.
- 10.6 Special features and application of DSO
- Practice: 1. Study of Lissajous pattern
 - 2. Measurement of phase of two signals
 - 3. Frequency comparison and to find the unknown frequency
 - 4. Measurement of voltage and frequency
 - 5. Component testing

164 48

Total

PRACTICE:

- 1. Extension of range of ammeter.
- 2. Extension of range of voltmeter.
- 3. Series ohmmeter.
- 4. Shunt ohmmeter.
- 5. FET Voltmeter.
- 6. Loading effect measurement.
- 7. Rectifier type instrument.
- 8. Measurement of input impedance.
- 9. Study of Lissajous pattern.
- 10. Measurement of phase of two signals.
- 11. Frequency comparison and to find the unknown frequency.
- 12. Measurement of voltage and frequency .
- 13. Component testing.

6. IMPLEMENTATION STRATEGY (PLANNING) :

- 1. Teaching Plan
- 2. Minimum 12 practicals/ assignments

7. REFERENCE BOOKS :

Sr.N o.	Author	Title	Publishers & Address
1.	W.D.Cooper	Electronic Instrumentation & Measurement Techniques Measurement	PHI, New Delhi
2.	Golding E.W.& Widdis	Electrical Measurement & Measuring Instruments	TMH, New Delhi
3.	Chiang H.H.	Electrical & Electronics Instrumentation	Dhanpatrai & Sons New Delhi
4.	Terman & Petit	Electronic Measurement	TMH, New Delhi

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: IV
Subject: Electronic Devices & Circuits -II	Code:IE-40
Group: C *	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks						
Th Hours per week	Tutorials Hrs. Per Wk.		Paper		TH	Sessional	T/W	Practical	Total
		Hrs	NP						
04	02	06	03	01	80	20	25	50	175

3. RATIONALE:

This course enables the students to understand symbol, construction, working & applications of devices like UJT, Thyristors, FET and Photoelectric devices. It also covers important applications of BJT like multi vibrators, Power amplifiers, and Tuned amplifiers.

4. OBJECTIVES:

After studying the subject the student will be able to

1. Understand the symbol, construction & characteristics of JFET, MOSFET, UJT, SCR, DIAC, TRIAC Photoelectric devices etc.
2. Understand and explain the applications of UJT, FET, SCR, DIAC, TRIAC Photoelectric devices etc.
3. Understand the need, working & application of power amplifiers & voltage tuned amplifier.
4. Understand & explain working of different BJT based multivibrators.
5. Design power amplifier, Schmitt trigger, Monostable multivibrator & C-S amplifier.

5. DETAILED CONTENTS:

Chapter	Content	Marks	Hours
	<u>SECTION – I</u>		
1.	1.0 Power Amplifier :	34	14
	1.1 Single ended and double ended amplifier		
	1.2 Class A transformer coupled amplifier		

- 1.2.1 Conversion efficiency
- 1.2.2 Harmonic distortion
- 1.2.3 O/P power
- 1.3 Push pull amplifier, class A,B, AB operation
- 1.4 Complementary symmetry push pull amplifier
- 1.5 Heat sink
- 1.6 Problems based on above topics
- 1.7 Design of class A,B power amplifiers
- 1.8 Calculation of heat sink requirements.

Practice :

- 1. Frequency response of single stage Transformer coupled amplifier.
- 2. Study of class AB push pull amplifier.

2.	2.0 Voltage Tuned Amplifiers : 2.1 Need 2.2 Single tuned voltage amplifier 2.3 Double tuned voltage amplifiers 2.4 Frequency response, B.W., Av and selectivity. Practice : Frequency response of single tuned voltage amplifier.	14	04
3.	3.0 FET : 3.1 Construction and char. of JFET 3.2 Effect of temp. 3.3 JFET parameters 3.4 Configurations 3.5 Small signal FET model 3.6 FET biasing 3.7 Common source and common drain amplifiers 3.8 FET applications as V.V.R. in AGC, constant current source etc.	34	14

- 3.9 Design of single stage C-S amplifier.
- 3.10 MOSFET :
Construction and char. of D-MOSFET and E-MOSFET, data sheet
- 3.11 Problems based on above topics.

Practice :

- 1. JFET characteristics.
- 1. MOSFET characteristics.
- 2. FET biasing ckts.
- 3. Frequency response of FET amplifier.

SECTION – II

4.	4.0 UJT : 4.1 Construction 4.2 Equivalent circuit. 4.3 V.I. characteristics 4.4 Applications of UJT 4.4.1 UJT relaxation oscillator 4.5 Frequency stability 4.6 UJT data sheet. Practice : 1. V.I. characteristics of UJT. 2. UJT relaxation oscillator.	12	04
5.	5.0 Thyristors : 5.1 Construction 5.2 Characteristics and Applications of SCR, DIAC, TRIAC 5.3 Analogy of SCR 5.4 Rating of above devices. Practice : 1. SCR characteristics. 2. Diac characteristics.	18	06
6.	6.0 Multivibrators :	36	16

- 6.1 Astable M.V.
- 6.2 Monostable M.V
- 6.3 Bistable M.V
- 6.4 Stable states
- 6.5 Triggering methods
- 6.6 Schmitt trigger
- 6.7 Applications of Schmitt trigger, Monostable, Astable & Bistable MVs.
- 6.8 Design of Schmitt trigger & monostable multivibrator using BJT.

Practice :

- 1. Study of astable M.V.
- 2. VCO (Symmetrical A.M.V.).
- 2. Bistable M.V.
- 3. Schmitt trigger.
- 4. Monostable M.V.

7.	7.0 Photoelectric Devices :	16	06
	a. Photoelectric effects		
	b. Construction and char. of photodiode, phototransistor, phototube, multiplier phototube, LED,LCD,		
	c. Optocoupler, photovoltaic cell and their applications		
	d. Burglar alarm.		
	Practice :		
	1. Phototube characteristics.		
	2. Burglar alarm.	164	64

Total

Two assignments on following design topics.

- i. Power amplifier
- ii. Multivibrator
- iii. JFET C-S amplifier

PRACTICE:

1. Study of V.I. characteristics of UJT.
2. Study of UJT relaxation oscillator.
3. Study of SCR characteristics.
4. Study of Diac characteristics.
5. Study of Schmitt trigger.
6. Study of FET characteristics.
7. Study of FET amplifier.
8. Study of Bistable Multivibrator.
9. Study of Voltage Controlled Oscillator.
10. Study of Frequency response of single tuned voltage amplifier.
11. Study of Push pull amplifier.
12. Study of Phototube characteristics.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals/assignments

7. REFERENCE BOOKS :

Sr.No.	Author	Title	Publishers & Address
1.	Millman and Halkias	Integrated Elex.	McGraw Hill International Edition
2.	Millman and Halkias	Electronic Devices and Circuits	McGraw Hill
3.	Robert Boylestad	Electronic Devices and Circuits	PHI, New Delhi
4.	Allen Mottershed	Electronic Devices and Circuits	PHI, New Delhi
5.	Millman & Taub	Pulse, digital and switching waveforms	TMH McGraw Hill
6.	Rammurthy	Thyristors and their application	East West New Delhi
7	Bhargava	Basic Elex. and linear ckts.	TTTI, Chandigadh TMH, New Delhi
8	M.M. Shah	Design of electronic circuits & computer aided design	Wiley Easten, Mumbai
9.	Sunil Shah	Design of Basic Electronic Circuits	Jeevandeep Prakashan

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: IV
Subject: Industrial Management-I	Code: ME-01
Group: M	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. Per Week	Practical Hrs. Per Week		Paper			TH	Sessional marks	T/ W	Pract	Total
		H rs	N /p	Mks						
03	-	03	3	1	100	80	20	-	-	100

3. RATIONALE:

Management is the process of getting work done through work force to achieve the objectives of the organization. This subject “Industrial Management-I” covers the fundamental principles, objectives, process and various activities of management.

Along with attaining the necessary technical knowledge, the technical students also require some inputs in management areas so as to enable them to carry out their work effectively and efficiently.

This subject will enable the students to understand various processes and activities in an industrial organization.

4. OBJECTIVES:

1. To understand the process of formation and promotion of Industry/Organization.
2. To understand the process of formation and promotion of industry /organization
3. To know the basic principles of supervision
4. To understand the Basics of Quality Management, Maintenance & Industrial Safety
5. To understand the principles and practices in Human Resource Management

5. DETAILED CONTENTS:

Chapter	Content	Marks	Hours
	<u>SECTION – I</u>		
1.	1.0 Introduction to Indian Industry 1.1 Meaning – Definition 1.2 Classification of Industry 1.3 Location of Industry 1.4 Importance of Industry 1.5 Resources for Industry	15	06
2.	2.0 Types of Ownership of Industry 2.1 Proprietorship 2.2 Partnership 2.3 Private Ltd Company 2.4 Public Ltd Company 2.5 Co-operative Enterprises 2.6 Public Sector Enterprises	17	04
3.	3.0 Supervisor & Supervision 3.1 Introduction 3.2 Role of Supervisor in an organization 3.3 Responsibilities of supervisor 3.4 Qualities of Supervisor 3.5 Skills of Supervisor 3.6 Functions of Supervisor	20	06
4.	4.0 Leadership 4.1 Meaning and nature 4.2 Qualities of good leaders 4.3 Types of Leadership 4.4 Supervisor's role as a leader	15	04
5.	5.0 Motivation 5.1 Meaning 5.2 Needs and expectations: Workmen	15	04

- 5.3Types of Motives: Primary & Secondary motives
- 5.4Affiliation motive, power motive and achievement motive
- 5.5Role of supervisor in motivating the workmen

SECTION – II

6.	6.0 Industrial Training	16	04
	6.1Need and importance of training		
	6.2Methods and techniques of training		
	6.3Training & Development of manpower		
	6.4Supervisor as a Trainer		
7.	7.0 Maintenance	16	04
	7.1Need for maintenance of industry		
	7.2Types of maintenance		
	7.3Maintenance and productivity		
	7.4Role of supervisor in shop floor maintenance		
8.	8.0 Quality Management	16	04
	8.1Meaning, purpose		
	8.2Areas of application and scope		
	8.3T.Q.M.		
	8.4ISO and its importance		
9.	9.0 Industrial Safety	16	06
	9.1Meaning and importance		
	9.2Accidents- causes of accidents		
	9.3Effects of Accidents		
	9.4Prevention of Accidents		
	9.5Safety devices and system		
10.	10.0 Industrial Relations	18	06
	10.1Meaning and importance		
	10.2Types of Industrial relations		
	10.3Industrial disputes-causes		
	10.4Methods and machinery for resolving industrial disputes		

10.5 Trade union-its role in maintaining industrial peace

TOTAL 164 48

Reference books:

Sr.No.	Author	Title	Publishers & Address
1.	T.R. Banga,S.C. Sharma	Industrial Organisation & Management	Khanna Publication
2.	O.P. Khanna	Industrial Management	Dhanpat Rai & Sons
3.	L.R. Bittal	What Every Supervisor should Know?	Tata Mc. Graw Hill

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME

TEACHING & EXAMINATION SCHEMES

V SEMESTER

Sr No	Subject Duration	Credit		Weekly Report	Quiz Test Marks	Dissertation (Report)	Oral/ Viva	Total
5.1	26 Weeks	15	Maximum Marks	50	50	50	50	200
			Minimum Marks	20	20	20	20	80

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME

SEMESTER: SIXTH TEACHING & EXAMINATION SCHEMES

Sr No	Subject	Subject Code	Scheme of Instructions and Periods per week					No.of papers, duration and marks			Scheme of Examination						Gr	Scheme L/P/Cr		
			L	P	D	T	Cr	NP	Hrs	Mks	SSL	Paper	T/W	PR	OR	Total				
1	#Elements of Comm.Engg	IE-66	4	2	-	-	6	1	3	100	20	80	25	-	50	175	A*	426		
2	#Principles of Instrumentation	IE-60	3	2	-	-	5	1	3	100	20	80	25	-	50	175	A*	325		
3	Principles of Control System	IE-67	3	2	-	-	5	1	3	100	20	80	25	-	50	175	A	325		
4	Signals & Systems	DE-62	3	2	-	-	5	1	3	100	20	80	25	-	50	175	A*	325		
5	Servicing Techniques	IE-61	1	3	-	-	4	-	-	-	-	-	50	50	-	100	M	134		
6	Industrial Electronics	DE-42	4	2	-	-	6	1	3	100	20	80	25	50	-	175	C*	426		
TOTAL			18	13	-	-	31	05 PAPERS			100	400	175	100	200	975				
TOTAL PERIODS = 31							TOTAL MARKS = 975													

* Compulsory

Award Winning subject

- (1) Unless stated, T.W. will be assessed by the Internal examiner based on considering the continuous assessment by the concerned internal faculty
- (2) Unless stated, Practical, Practical/Oral (P/O), oral will be assessed jointly by the internal and external examiners.

L- Lecture Period

P- Practical Period

D-Drawing Practice Period

NP- No.of Papers

Mks - Marks

SSL- Sessional

T/W- Term Work

Pract- Practical

Cr- Credit

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: VI
Subject: Elements of Communication Engg.	Subject Code: IE-66
Group: A*	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks						
Theory Hrs. per week	Practical Hrs per week		Paper		Th	Sessional	T/W	Oral	Total
			Hrs.	Np					
04	02	06	03	01	80	20	25	50	175

3. RATIONALE :

This course enables the students to understand fundamentals of telecommunication. The knowledge of communication is essential in the world of information Technology.

4. OBJECTIVES :

After studying the subject the student will be able to

6. Explain the analog and digital modulation techniques.
7. Draw the circuits for radio and analyse the waveforms.
8. Identify the need for multiplexing communication signals.
9. Draw the radiation pattern for antenna.
10. Understand T.V. and data communication.

6. DETAILED CONTENTS :

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Introduction to communication system: 1.1 Block diagram of communication system 1.2 Need for modulation 1.3 Bandwidth requirements	02	01
2.	2.0 Amplitude Modulation: 2.1 Amplitude modulation theory 2.1.1 Frequency spectrum of AM Wave 2.1.2 Representation of AM Wave 2.1.3 Power relation in AM Wave 2.1.4 Problem based on AM Wave 2.2 Generation of AM Wave 2.2.1 Basic requirement 2.2.2 Grid modulation class 'C' Amplifier 2.2.3 Plate modulation class 'C' Amplifier 2.2.4 Collector modulation 2.2.5 AM transmitter block diagram	14	06
3.	3.0 Single sideband technique: 3.1 Suppression of carrier 3.1.1 Effect of non linear resistance on added signals 3.1.2 Balance modulator 3.2 Suppression of unwanted sideband 3.2.1 The filter system 3.2.2 Phase shift method	09	03
4.	4.0 Frequency modulation: 4.1 Theory of frequency and phase modulation 4.1.1 Mathematical representation of FM wave 4.1.2 Frequency spectrum of FM Wave 4.1.3 Phase Modulation	14	06

4.2	Noise and frequency modulation		
4.2.1	Effect of noise on carrier noise triangle		
4.2.2	Pre emphasis and de emphasis		
4.2.3	Comparison of Wide Band and Narrow Band FM		
4.3	Generation of frequency modulation		
4.3.1	Direct method		
4.3.2	Stabilized reactance modulator-AFC		
4.3.3	Indirect method		
5.	5.0 Radio receiver:	15	05
	5.1 Receiver type		
	5.1.1 Tuned radio frequency(TRF) receiver		
	5.1.2 Superhetrodyne receiver		
	5.2 AM receiver		
	5.2.1 RF section and characteristics		
	5.2.2 Frequency changing and tracking		
	5.2.3 Intermediate frequency and IF amplifier		
	5.2.4 Detection and automatic gain control(AGC)		
	5.3 FM receiver		
	5.3.1 Amplitude limiting		
	5.3.2 Basic AM demodulator		
	5.3.3 FM detectors frequency and phase discriminators		
6.	6.0 Pulse communication:	06	03
	6.1 Introduction-types		
	6.2 Pulse width modulation-PWM		
	6.3 Pulse position modulation-PPM		
	6.4 Pulse code modulation-PCM		
7.	7.0 Multiplexing Schemes:	06	02
	7.1 Types-frequency division and time division		
8.	8.0 Noise:	08	03
	8.1 Types of noise		
	8.2 Noise calculation		
	8.3 Noise figure, noise temperature		

	8.4 Problem based on above topics		
9.	9.0 Phase locked loop:	08	03
	9.1 Block diagram		
	9.2 Study of PLL IC 565		
	9.3 Applications		
	<u>SECTION-II</u>		
10.	10.0 Radiation and Propagation of Wave :	16	06
	10.1 Electromagnetic Radiation		
	10.1.1 Fundamental of EM Wave		
	10.1.2 Effect of environment		
	10.2 Propagation of Wave		
	10.2.1 Ground Wave		
	10.2.2 Sky Wave Propagation-Ionosphere		
	10.2.3 Space Wave		
	10.2.4 Tropospheric Wave Scatter Propagation		
11.	11.0 Antenna:	22	08
	11.1 Radiation Mechanism		
	11.2 Wire Radiator in space		
	11.2.1 Current and Voltage distribution		
	11.2.2 Resonant antenna		
	11.2.3 Non-Resonant antenna		
	11.3 Terms and definitions		
	11.3.1 Antenna gain		
	11.3.2 Antenna resistance		
	11.3.3 Bandwidth, Beamwidth and Polarization		
	11.4 Effect of Ground on antenna		
	11.5 Directional high frequency antenna		
	11.5.1 Dipole arrays		
	11.5.2 Folded dipole and applications		
	11.5.3 Non Resonant antenna-The Rhombic antenna		
	11.6 Microwave antenna		
	11.6.1 Antenna with parabolic reflection		

	11.6.2 Horn antenna		
	11.7 Wideband and special purpose antenna		
	11.7.1 Helical antenna		
	11.7.2 Loop antenna		
12.	12.0 Monochrome Television :	20	09
	12.1 Picture transmission		
	12.2 TV transmitter		
	12.3 TV receiver		
	12.4 Synchronization		
	12.5 Aspect ratio		
	12.6 Scanning		
	12.7 Picture resolution		
	12.8 Composite video signal		
	12.9 VSB(Vestigial Sideband)		
	12.10 Interference suffered by TV signal		
	12.11 TV broadcast channel		
	12.12 Monochrome TV receiver		
13.	13.0 Colour Television:	18	07
	13.1 Additive colour mixing		
	13.2 Luminance signal		
	13.3 Compatibility		
	13.4 Colour difference signal		
	13.5 Chrominance signals		
	13.6 PAL-Encoder		
	13.7 PAL-D-Colour receiver		
	13.7.1 Chroma Decoder		
14.	14.0 Digital Television: Transmission and Reception	06	02
	14.1 Digital satellite TV		
	14.2 Direct to home digital satellite TV		
	TOTAL	164	64

Practice:

1. Study of AM Modulation
2. AM detector.
3. Audio Amplifier.
4. AM receiver/transmitter.
5. Pulse Width Modulation.
6. Frequency modulation.
7. F.S.K
8. P.S.K
9. Study of composite video signal.
10. Monochrome T.V. receiver.
11. Study of Antenna Radiation Pattern.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals/assignments in the term

7. REFERENCE BOOKS :

Sr.No.	Author	Title	Publishers & Address
1.	George Kennedy	Electronic Communication System	TMH
2.	Taub and Schilling	Principles of Communication System	TMH, New Delhi
3.	Wayne Tomasi	Advance Electronic Communication	PHI, New York
4.	R.R. Gulati	Monochrome & Colour T.V.	Wiley Eastern Bombay

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: VI
Subject: #Principles of Instrumentation	Subject Code: IE-60
Group: A*	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. per week	Practical Hrs. per week		Paper			Th	Sessional	T/ W	Oral	Total
			Hr s	N P	Mks					
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE :

It is necessary to fully understand the system under investigation before suggesting suitable transducers and then utilize the electronic support. It is also necessary to know the basic principles of transducers and show how each of them can be used for the measurement of large number of variables.

4. OBJECTIVES :

After studying the subject the student will be able to

1. Select proper transducer for particular application.
2. Use proper signal conditioning circuit.

Design a complete system for the measurement of non electrical quantities.

5. DETAILED CONTENTS :

SECTION-I

Chapter	Content	Marks	Hours
1.	1.0 Instrumentation : Fundamentals, Instrumentation terminology: error, Accuracy, Precision, Sensitivity, Reliability, Sources of error in instruments.	12	02
2.	2.0 Transducers and primary sensors :		
	2.1 Transducers in measurements. Transducer: actuating mechanism, types of transducers voltage and current generating transducer, piezoelectric, photoelectric, thermoelectric, magnetoelectric, variable parameter analog transducers, frequency generating transducers, transducer selection factors.	10	03
	2.2 Applications of the above transducers for measurement of speed, displacement, acceleration, vibration, strain, pressure, level, temperature, flow, PH and conductivity.	36	12
	2.3 Transducers for Biomedical application. Instrumentation for clinical laboratory.	24	07
	Practice:		
	1. Measurement of displacement.		
	2. Thermocouple characteristics.		
	3. Thermistor characteristics.		
	4. RTD characteristics.		
	5. Strain guage.		
	6. Photo transducer		

SECTION-II

3.	3.0 Amplifiers and signal conditioning : 3.1 Input modification, balance bridges inputs 3.2 Instrumentation amplifiers, 3.3 chopper modulators, solid state modulators and demodulator. 3.4 Analog and digital measurements, A/D and D/A conversion 3.5 Isolation and current loop transmitters. Practice: 1 Instrumentation amplifier. 2 Diode chopper modulator. 3 Analog to digital converter. 4 Digital to analog converter 5 V to I and I to V converter. 6 Voltage to frequency converter. 7 Study of comparator	32	10
4.	4.0 Indicating and recording devices : 4.1 Analog indicators, oscillograph and graphic recorders 4.2 Numerical and digital indicators 4.3 Data acquisition system 4.4 printers Practice: 1. Digital panel meter	16	04
5.	5.0 Systems for control and measurement of displacement, speed, acceleration, pressure, level temperature etc. using above blocks. Practice: 1. Assignment on system design	16	04
6.	6.0 Interference signals : Capacitive interference, inductive interference and shielding, conductively coupled interference, ground loop	12	04

interference.

Practice: 1.Assignment on interference signals

7.	7.0 Signal to noise considerations : Fluctuation and noise in measurement system. Sources of noise. Practice: 1.Assignment on signal to noise considerations	06	02
	TOTAL	164	48

PRACTICE:

1. L.V.D.T.
2. Photosensor.
3. Strain guage.
4. Thermocouple characteristics.
5. Thermistor characteristics.
6. V to I and I to V converter.
7. Diode chopper modulator.
8. Instrumentation amplifier.
9. IC 7107/ IC 7106 .
10. Voltage to frequency converter.
11. Quad comparator IC LM 339.
12. Study of DAC 0808.
- 13.RTD characteristics.
14. Study of ADC.
- 15.Study of Digital panel meter.
16. Solid state temperature transducer.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals/assignments in the term

7. REFERENCE BOOKS :

Sr.No.	Author	Title	Publishers & Address
1.	A.K. Sawhney	A course in electrical and electronic measurement & instrumentation	Dhanpatrai & Sons, New Delhi.
2.	Curtis Johnson	Process control instrumentation technology	PHI, New Delhi.
3.	H.S. Kalsi	Electronic instrumentation	TMH, New Delhi.
4.	Joseph Carr John M.Brown	Introduction to bio-medical equipment technology	Pearson Education, (Singapore) Pte Ltd, New Delhi.
5.	StanleyWolf Richard Smith	Students reference manual	PHI, New Delhi.

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: VI
Subject: Principles of Control System Group: A	Subject Code: IE-67 Optional

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. per week	Practical Hrs. per week		Paper			Th	Sessional	T/W	Oral	Total
			Hr s	N P	Mks					
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE :

This subject helps to develop understanding of concept like stability of a system; and how feedback is an important technique of improving characteristics of system.

4. OBJECTIVES :

After studying the subject the student will be able to

2. Understand concept of closed loop system.
3. Understand concept and techniques of time domain analysis.
4. Understand concepts and techniques of frequency domain analysis.
5. Understand working of servo components.

5. DETAILED CONTENTS :

		<u>SECTION-I</u>		
Chapter	Content		Marks	Hours
1.	1.0 Control System : 1.1 Open loop control system and closed loop control system. 1.2 Elementary form of control system. 1.3 Block diagram representation of system. 1.4 Transfer function of the system, effect of feedback on control system.		20	08
2.	2.1. Introductory ideas of laplace transforms and its use in solving differential equation. 2.2 Description of time domain behaviors of control system. 2.3 Steady state and transient response. Time domain specifications. Characteristics equation of the system. 2.4 Roots of the equation and system response. 2.5 Steady state error analysis problems. Test Signals .Type of the system.		44	18
3.	3.0 Stability : 3.1 Stability of the system – absolute stability, relative stability, 3.2 Routh's criteria, 3.3 Nyquist criteria – problems. Practice:		18	06
		<u>SECTION-II</u>		
4.	4.1 Description of frequency domain behavior of control system. 4.2 Frequency domain specification. Bode diagram.		18	06
5.	5.0 Study of servo components :		40	18

5.1 Potentiometers, synchro elements- construction, principle of operation, applications.

5.2 Simple theory of servomechanism – A.C. position servomechanism D.C. position servomechanism. A.C. servomotor, D.C. servomotor – advantages and disadvantages.

5.3 Amplidyne – Ward Leonard system of speed control.

5.4 Servo amplifier – servo modulators and demodulators.

5.5 Stepper motor applications. System examples – Digital servo system, Voltage regulator system.

Practice:

1. Synchro characteristics.
2. Synchro pair as an error detector.
3. Potentiometer characteristics.
4. Potentiometer pair as an error detector.
5. Characteristics of A.C. servomotor.
6. Stepper motor.
7. Phase sensitive demodulator.
8. Compensatory networks – Lag - Lead.
9. Compensatory networks – Lead – Lag.
10. Study of D.C. positional servo system.
11. Study of A.C. positional servo system.

6.	6.1 Effect of gain on the system performance	24	08
	6.2 Effect of derivative control & integral control.		
	6.3 Compensation networks – lag lead, effect of Tachometer rate feedback control.		
	Total	164	48

PRACTICE:

1. Synchro transmitter.
2. Synchro pair as an error detector.
3. Compensatory networks – Lag .
4. Compensatory networks – Lead .
5. Linear Potentiometer characteristics.
6. Characteristics of A.C. servomotor.
7. Transistor modulator.
8. Phase sensitive detector.
9. Stepper motor.
- 10 .D.C. positional servo system.
11. Study of A.C. positional servo system.
12. Study of D.C. positional servo system.
13. Characteristics of A.C. servomotor.
14. Potentiometer pair as an error detector.

6. IMPLEMENTATION STRATEGY (PLANNING)

3. Teaching Plan
4. Minimum 12 practicals/assignments in the term

7. REFERENCE BOOKS :

Sr.N o.	Author	Title	Publishers & Address
1.	S.C.Goyal and U.A.Bakshi	Principles of Control System	Technical Publication, Pune
2.	Benjamin C. Kuo	Automatic Control System	PHI
3.	I. J. Nagrath/ M. Gopal	Control System Engineering	Wiley Eastern Ltd.(New Delhi)

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics Subject: Signals and Systems Group: A	Semester: VI Code: DE-62 Compulsory
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2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. per week	Practical Hrs. per week		Paper			Th	Sessional	T/W	Oral	Total
			Hrs	NP	Mks					
03	02	05	03	01	100	80	20	25	50	175

3. RATIONALE:

The subject of Signals and Systems has assumed great importance in the recent time because of increasing practical use of signal analysis in Digital Signal Processing(DSP). This field finds practical applications in speech analysis, processing and compression. Particularly in medical field (ultra sound imaging, CT scan). Signal processing also finds extensive applications in telephony, radar, sonar and space probes.

4. OBJECTIVES:

After studying this subject the students will be able to:

1. Analyze different signals.
2. Find fourier analysis of continuous and discrete time signals.

5. DETAILED CONTENTS:

Section I			
		Hrs.	Marks
1.	Introduction to signals <ul style="list-style-type: none">- Discrete and continuous signals- Transformation of signals, the need and requirements- Standard test signals unit step and unit impulse in both domain.- Periodicity concepts.	04	16
2.	System <ul style="list-style-type: none">- Introduction and overview of a system- Properties of system like<ol style="list-style-type: none">1.Memory2.Invertibility3. Causality4. Stability5. Time invariance6. Linearity	04	18
3.	Linear time invariant system(LTI) <ul style="list-style-type: none">- Importance of LTI system1. Signals in terms of impulse of signals2. Convolution sum of LTI discrete systems3. Convolution integral of LTI continuous systems4. Properties of LTI system5. Unit step response of LTI system6. Representation of LTI system using differential and difference equations7. Block diagram representation of systems	16	48

Section II			
4.	Continuous time signals and systems(CT) - Fourier analysis 1. The response CT LTI system to complex exponentials 2. Fourier series representation of CT LTI periodic signals 3. Fourier series representation of periodic signals (Fourier Transforms) convergence of Fourier transforms 4. Properties of Fourier transforms Parseval's equation 5. Frequency response of systems by linear constant coefficient differential equation. Typical example of first order and second order system.	12	40
5.	Fourier representation of discrete time LTI system 1. The response of discrete time LTI system to complex exponentials(discrete time Fourier transforms DTFT) 2. Representation of periodic signals using DTFT 3. Properties of DTFT 4. Duality of DTFT and Fourier series coefficient 5. Frequency response of LTI system characterized by difference equation	12	42
Total		48	164

PRACTICE:

1. Study of different signals.
2. Assignments on different types of systems.
3. Problems on
 - a. LTI system.
 - b. Convolution.

- c. Fourier series .
- d. Fourier transform.
- e. DTFT

6. IMPLEMENTATION STRATEGY (PLANNING) :

- 6. Teaching Plan
- 7. Minimum 12 practicals/assignments in the term

7. REFERENCE BOOKS:

Sr. No	Author	Title	Publishers & Address
1.	Oppenheim Wilsky and Young	Signals and Systems	PHI
2.	Gable and Roberts	Signals and Systems	Wiley Publications
3.	Simon Haykin and Barry Van Veen	Signals and Systems	John Wiley
4.	Benoit Boulet	Fundamentals of Signals and Systems	Dreamtech
5.	Smarajit Ghosh	Signals and Systems	Pearson Education

1. Subject Details:

Course: Diploma in Digital Electronics	Semester: VI
Subject: Servicing Techniques	Subject Code: IE-61
Group: M	Optional

2. Teaching and Examination Scheme:

Teaching Scheme		Credits	Examination Scheme & Maximum Marks							
Theory	Practical		Paper			Th	Sessional	T/W	Practical	Total
Hrs./week	Hrs/week		Hrs	Np	Mks					
01	03	04	-	-	-	-	-	50	50	100

3. Rationale:

This subject will develop the basic skill of maintenance of equipments. It will also provide the necessary knowledge and competence in finding systematic repair of electronic test equipment.

It also provides the information of maintenance management of service department/service enterprise in industry.

4. Objectives:

Student will be able:

1. To develop tracing, testing maintenance and trouble shooting knowledge.
2. To do the analysis of circuit.
3. To develop different fault finding techniques like visual inspection, tracing, voltage and signal analysis.
4. To find the equivalent components with the help of data book
5. Do effective use of internet.

6. Do effective use of computer, operation manual and service manual.

5. Contents:

Chapter	Name of the topic	Hours
01.	Reliability aspects of electronic equipments <ul style="list-style-type: none"> • Traditional bath tub reliability curve • Generalized reliability curve • Mean time to fail • Failure rate • Mean time between failure • Mean time to repair • Mean time to restore time • Thermal acceleration • Practical reliability • Quality standards • Maintenance policy • Preventive maintenance • Corrective maintenance • Qualitative maintenance 	04
02.	Maintenance Management <ul style="list-style-type: none"> • Maintenance policy • Equipment service option • Types of contract • General contract provision • Maintenance organization • Training maintenance personnel • Planning of spare parts inventory • Assigning spare parts requirement • Essentials of good equipments management program • Acquisition program 	02

	<ul style="list-style-type: none"> • Planning of utilities • Incoming inspection • Inventory control • User training • Technical training • Management of service manual and reference library • Maintenance arrangement • Calibration check • Preventive maintenance • ALERT issue • Quality assurance • Installation procedure • Environmental conditions • Humidity • Shock and vibrations Protection from electromagnetic interference • Safety • Service and Maintenance laboratories • Work bench • Lighting • Storage • Maintenance system overview • Log book • Performa for recording specification • Performa for preventive maintenance • Performa for corrective maintenance • Data analysis • Information tags • Personal safety 	
03.	Fundamental Troubleshooting Procedure	02

	<ul style="list-style-type: none"> • Reading of block diagram • Reading of circuit diagram • De-assemble • Re-assemble • Trouble shooting process • Fault establishment • Fault correction • Fault finding aids • Service, Maintenance and Instruction manual • Test and Measuring tools • Pre troubleshooting techniques • Preliminary Observation • Function area approach • Split half method • Divergent path • Convergent path • Feedback path • Systematic troubleshooting checks • Check control setting • Check associated equipments • Visual check • Calibration • Isolate the troubling circuit • Measurement • Individual component 	
04.	Troubleshooting Procedure 1. Visual inspection 2. Measure <ul style="list-style-type: none"> • Voltage levels • Presence of signal waveform 	02

	3. Component failure <ul style="list-style-type: none"> • Out of circuit test • In circuit test 4. Signal tracing 5. Functional analysis	
05.	Troubleshooting Aids Tools <ul style="list-style-type: none"> • Service manual • Circuit diagram • Circuit board location • Voltage analysis • Fault finding flow check • Diagnostic software • List of replaceable parts 	02
06.	Data Manuals Troubleshooting Technique <ol style="list-style-type: none"> 1. Voltage analysis 2. Signal injection 3. Signal tracing 	02
07.	Computer Maintenance <ul style="list-style-type: none"> • Need of preventive maintenance • Assembling of PC. • Installation of O.S. • Trouble shooting • Hardware: Peripherals • Software 	02
	Total	16

Practice:

1. Testing of resistor, capacitor and inductance by using multimeter, LCR Q meter and CRO.
2. Testing of transistor by using multimeter, transistor tester and CRO.
3. Testing of IC using IC tester.
4. Testing of variable resistor, connectors, switches by using multimeter.
5. Testing of diode, zener diode, varactor diode, Photo diode, Tunnel diode, LDR, thermistor, 7 segment display, FET, MOSFET, SCR, Triac with the help of multimeter.
6. Layout of components in given power supply
 - Tracing of voltage regulation section in given power supply
 - Voltage analysis in given power supply
 - Fault finding in given power supply by voltage analysis method.
7. Layout of components for given function generator
 - Tracing of alternation section used in function generator
 - Voltage analysis in given function generator
 - Fault finding in function generator by voltage analysis method.
8. Layout of components for given CRO
Tracing a vertical section of CRO.
9. Fault finding of memory, connector and power supply in PC.

Assignment: List of different tools and materials with specifications.**6. Implementation Strategy (Planning):**

1. Teaching Plan
2. Minimum 10 practicals / assignments.

7. Reference Books:

S. No.	Author	Title	Publication
1.	R.G. Gupta	Electronic instruments & systems	TMH
2.	R.S. Khandpur	Trouble shooting of Electronic equipment	TMH
3.	Scott Muller	Upgrading and Repairing of PC	Pearson Education

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester: VI
Subject: Industrial Electronics	Code: DE-42
Group: C*	Compulsory

2. TEACHING & EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme & Maximum Marks							
Theory	Practical		Paper			Th	Sessional	T/w	Practical	Total
Hrs./week	Hrs/week		Hrs	Np	Mks					
4	2	6	3	1	100	80	20	25	50	175

3. RATIONALE:

This course enables the students to learn basic applications of power control devices in digital systems. It also puts stress on use of microprocessor in control of drives.

4. OBJECTIVES:

After studying this subject students will be able to

- 1) Design a Regulated power supply
- 2) Control the role of SCR for a particular interval of time
- 3) Design an inverter for industrial applications
- 4) Control speed of motor
- 5) Make use of thyristers in microprocessor based system for control strategy

6. DETAILED CONTENTS :

		<u>SECTION- I</u>		
Chapter	Content		Marks	Hours
1	1.0.Regulated Power Supplies		24	10
1.1	Series and shunt regulators			
1.2	Overload and Overvoltage protection			
1.3	Crowbar protection			
1.4	Use of voltage regulated IC 's such as LM 723 LM 309,78xx series			
	Practice : 1. Series Regulator 2. Voltage Regulator using LM 723			
2.	2.0 Thyristers and Power devices		12	04
	2.1 Silicon control rectifier (SCR)			
	2.2 Triac, Silicon Controlled switch (SCS)			
	2.3 Insulated Gate bipolar Transistor (IGBT)			
	2.4 Gate turn-off thyrister (GTC)			
	2.5 Metal Oxide Controlled Thyrister (MCT)			
	2.6 Emitter turn-off (ETO)			
	2.7 Power MOSFET.			
	Practice : 1. Triac Characteristic			
3.	3.0 SCR controlled Rectifiers		20	08
	3.1 Turn ON and Turn OFF circuits for SCR			
	3.2 SCR controlled rectifier treatment for resistive and inductive load.			
	Practice : 1. Class 'C' commutation 2. Triac phase Control 3. Single phase fully controlled converter			
4.	4.0 Inverters			
	4.1 Brief introduction to series, parallel and bridge inverter		26	10

Practice : 1. Assignment on Inverter

SECTION-II

5.	5.0 Industrial circuits	16	06
	5.1 Battery charge regulator		
	5.2 Temperature controller using SCR		
	5.3 AC voltage stabilizer		
	5.4 Sawtooth generator		
	5.5 Light Dimmer Circuit		
	5.6 Electronic timer		
	Practice 1. SCR-UJT light Dimmer		
	2. Diac- Triac light Dimmer		
	3. DC Timer		
	4. Pulse width Modulation		
6.	6.0 Control of DC motor	14	04
	6.1 DC motor relation		
	6.2 Speed regulation by armature Voltage Control		
	Practice : 1. Assignment on d.c. motor		
7.	7.0 SMPS	22	10
	7.1 Concept of SMPS		
	7.2 Classification of SMPS		
	7.3 Study of standard design of SMPS		
	7.4 Study of PWM IC TL 494		
	Practice : 1 Study of TL 494		
8.	8.0 Uninterrupted Power Supply	18	08
	8.1 Introduction to UPS		

8.2 Block diagrams of UPS
8.3 Study of standard designs of UPS
Practice : 1. Assignment on UPS

9.	9.0 Microprocessor based & Microcontroller Based DC drives	12	04
	Practice : 1. Assignment on Microprocessor based Based DC Drives		
	Total	164	64

PRACTICE:

1. Study of DC timer.
2. Study of Series regulator.
3. Study of 723 regulator.
4. Study of Constant voltage/constant current sweep generator.
5. SCR UJT light dimmer.
6. Phase control of triac.
7. Sequential timer.
8. Diac-triac light dimmer circuit.
9. PUT relaxation oscillator.
10. Ramp and pedestal control circuit.
11. Class C commutation.
12. R & RC triggering of SCR.
13. 78xx Series Regulator.

6. IMPLEMENTATION STRATEGY (PLANNING) :

2. Teaching Plan
2. Minimum 12 practicals or assignments

7. REFERENCE BOOKS :

Sr.No	Author	Title	Publishers & Address
1.	P.C. Sen	Power Electronic	TMH,New Delhi
2.	Goyal,Khetan	A monograph on electronics Design Principle	Khanna Publishers Delhi
3.	Chute and Chute	Electronics in Industry	MGH New Delhi
4.	Ramamurthy	Thyrister & its applications	East West Press,New Delhi

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal's
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME
SEMESTER: SEVENTH TEACHING & EXAMINATION SCHEMES

Sr No	Subject	Subject Code	Scheme of Instructions and Periods per week					No.of papers, duration and marks			Scheme of Examination						Gr	Scheme L/P/Cr			
			L	P	D	T	Cr	NP	Hrs	Mks	SSL	Paper	T/W	PR	OR	Total					
1	#Adv. Processor & PC Architecture	DE-72	4	2	-	-	6	1	3	100	20	80	25	50	-	175	A*	426			
2	#Telematics	DE-77	4	2	-	-	6	1	3	100	20	80	25	-	50	175	A*	426			
3	#Computer Networks	DE-76	4	2	-	-	6	1	3	100	20	80	25	-	50	175	A*	426			
4	#Project	DE-70	1	6	-	-	7	-	-	-	-	-	50	-	50	100	A*	167			
5	#Industrial Management-II	ME-02	3	-	-	-	3	1	3	100	20	80	-	-	-	100	M*	303			
6	#Elective (Any One)																				
	i) Robotics	IE-73	4	2	-	-	6	1	3	100	20	80	25	-	50	175	A*	426			
	ii) Embedded System Design	DE-79	4	2	-	-	6	1	3	100	20	80	25	-	50	175	A*	426			
	TOTAL		20	14	-	-	34	05		PAPERS	100	400	150	50	200	900					
			TOTAL PERIODS = 34							TOTAL MARKS = 900											

* Compulsory

Award Winning subject

- (1) Unless stated, T.W. will be assessed by the Internal examiner based on considering the continuous assessment by the concerned internal faculty
- (2) Unless stated, Practical, Practical/Oral (P/O), oral will be assessed jointly by the internal and external examiners.

L- Lecture Period

P- Practical Period

D-Drawing Practice Period

NP- No.of Papers

Mks - Marks

SSL- Sessional

T/W- Term Work

Pract- Practical

Cr- Credit

1. SUBJECT DETAILS :

Course : Diploma in Digital Electronics
Subject : #Advanced Processor and PC Architecture
Group : A *

Semester : VII
Code: DE-72
Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination scheme and Maximum marks							
Theory Per Week	Practical Per Week		Paper			Th	SSL	T/W	PR	TOTAL
			Hrs	NP	Mks					
4	2	06	03	01	100	80	20	25	50	175

3. RATIONALE :

Since the introduction of IBM PC, the microprocessor market has matured significantly. Most of the important advances in the computer system technology have been closely related to the development of high performance microprocessors. These high performance microprocessors have become more widely used in the design of new electronic equipments and computers. Hence, there is a need to keep updated with the advanced processors.

4. OBJECTIVES :

After studying the subject student will be able to

1. Write assembly language programmes using Intel 8086/80486 instruction set
2. Design systems using peripheral integrated circuits
3. Understand functioning of Personal Computer

5. DETAILED CONTENTS :

SECTION-I

	Hours	Marks
1. Intel 8086	14	34
1.1 Architecture		
1.2 Memory organization		
1.3 Instruction set		
1.4 Assemble language programming		
1.5 Interrupt structure		
1.6 Timing diagrams		
1.7 Minimum mode and maximum mode		
Practice: 1. To execute assembly language programmes using Intel 8086 instruction set (Min.10 programmes)		
2. System design using, 8086 microprocessor (in minimum and maximum mode)	06	16
Practice: 1. Assignment- System design using Intel 8086 Microprocessor in minimum and maximum mode		
3. Study of 80486 Microprocessor	12	32
3.1 Architecture		
3.2 Task switching		
3.3 Protection		
3.4 Memory management		
Practice: 1. Programs based on 80486.		

SECTION-II

4. Advances in Processor Architecture	12	32
4.1 Pentium-pro, Core 2 Duo, i-7 Core.		
Practice: Study of Motherboards.		

5. Study of PC Architecture	08	24
5.1 Chipset study-North/ south Bridge GMCH/10CH/FWH (Firmware Hub)		
5.2 Interfaces: PCI/PCI Express PATA /SATA USB 1.0/2.0 SCSI/SAS		
5.3 Memory: DDR/DDR 2/DDR3 Bank concept Error checking and correction		
Practice: Study of Bus.		
6. Study of Advanced Technology.	08	18
6.1 Hyperthreading		
6.2 Virtualization technology		
6.3 Expanded memory technology, (EM-64T),		
6.4 TPM(LGT)		
6.5 Active Management Technology		
6.6 Multicore Technology Processor		
6.7 Power Management (Energy Efficiency).		
Practice: Virtualization technique.		
7. Servicing & Maintenance.	04	08
Practice: Servicing of PC.		
	Total	
	64	164

PRACTICE:

1. To execute assembly language programs using Intel 8086 instruction set (Min.08 programs).
2. Assignment- System design using Intel 8086 Microprocessor in minimum and maximum mode.
3. Programs based on 80486.
4. Study of Motherboards.
5. Study of Bus.
6. Virtualization technique.

7. Servicing of PC.

6. IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Minimum 12 practicals/assignments.

7. REFERENCE BOOKS :

Sr.N o.	Author	Title	Publishers
1.	Douglas V. Hall	Microprocessor interfacing Programming and Hardware	Tata Mc-Graw Hill
2.	Liu Gibson	8086, 80186, 80286 Microprocessor	PHI, New Delhi
3.	Intel Data Sheets	-	Intel Corporation USA

Web Reference: www.intel.com
www.ibm.com

1. SUBJECT DETAILS:

Course : Diploma in Digital Electronics	Semester : VII
Subject : #Telematics	Code: DE-77
Group : A*	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. per week	Practical Hrs. per week		Paper			Th	Sessional	T/W	Oral.	Total
		Hrs.	Np	Mks						
04	02	06	03	01	100	80	20	25	50	175

3. RATIONALE:

Telecommunication is undergoing radical changes in different areas like Terminal Transmission Systems and Switches. Computer communications through Internet and Cellular Phones have changed the whole scenario of communication and has brought the whole world very close. Hence there is a need for keeping updated with the latest developments in the field of transmission of voice and data. This subject deals with various aspects of voice and data communication.

4. OBJECTIVES:

After studying the subject students will be able to:

1. Draw and analyze the circuit of simple telephone, cordless phone, cell phone.
2. Design and draw digital switching network.
3. Identify the role of ISDN and 7-layered ISO-OSI model.
4. List microwave applications.
5. Explain the cell phone technology.

5. DETAILED CONTENTS:

SECTION 1

Contents	Marks	Hrs
1. Block diagram & working of Telephone Instruments: Push button telephone Cordless telephone Cell phone Practice: Study of Telephone instruments	12	04
2. Voice digitization: Nyquist sampling rate Quantization Signal to quantization Noise ratio Companding (A-law, u-law) Encoding & decoding of PCM signals CODEC Delta modulation, Adaptive Delta modulation DPCM Practice: 1. Study of linear PCM 2. Study of companding using CODEC 3. Differential pulse code modulation 4. Linear delta and adaptive delta modulation 5. Study of time division multiplexing	20	08

3. Signalling:	10	04
Line and Register signals on loops		
Line and Register signals on trunks		
Common Channel signaling		
Practice:		
Assignment on Signalling		
4. Digital Switching:	18	06
Digital Space and Time Switch		
Time Space		
Space Time Space Switch		
Stored Program Control		
Practice:		
Assignment on Digital Switching		
5. Introduction to Mobile Communication:	22	10
Cellular concepts		
Multiple access technologies		
Cellular system operation		
System architecture		
Location updating & call setup		
Hanoff & power control		
Digital cellular mobile		
GSM- N/w architecture, Service aspects,		
Radio aspects & Security aspects		
5.4.2 CDMA- N/w architecture, Service aspects,		
Radio aspects & Security aspects		

SECTION-2

6. Data Communication:	18	10
Data types		
Topology		
Error detecting and correcting codes		
Digital Modulation: PSK, QPSK, QAM		
MODEM		
Switching circuit, message, packet switching		
Practice:		
1. Study of Digital Modulation: PSK, QPSK, QAM		
2. Study of MODEM		
7. Study of EPABX	08	02
Practice:		
Study of EPABX		
8. ISDN: Integrated Service Digital Network:	12	02
Objectives		
Architecture, User-N/W interface		
ISO-OSI layered model		
BSDN		
Practice:Assignment on ISDN		

9. Fibre Optic Communication:	20	08
9.1 Introduction		
9.2 Types of optical fibre		
9.3 Transmission characteristics- Attenuation, Dispersion		
9.4 Optical sources- LASER, LED		
9.5 Optical detectors-photodiode, avalanche photodiode		
9.6 WDM		

Practice:

- 1) Study of fibre optic communication
 - 2) Study of source characteristics
 - 3) Study of detector characteristics
 - 4) Measurement of NA; attenuation
- Assignments- Problems based on above topic

10. Microwave Communication:	24	10
10.1 Microwave waveguides and components		
10.1.1 Rectangular waveguides		
10.1.2 Circular waveguides		
10.1.3 Cavity resonators		
10.1.4 Waveguide coupling, matching & attenuation		
10.1.5 Directional couples, circulators & isolators		
10.2 Microwave tubes and devices:		
Klystron, Magnetron, TWT, Gunn diode		
Practice:		
1) Problems based on the above topics		
2) Experiments based on microwave bench		

Total 164 64

PRACTICE:

1. Study of Telephone instruments
2. Study of linear PCM
3. Study of companding using CODEC
4. Differential pulse code modulation
5. Linear delta and adaptive delta modulation
6. Study of time division multiplexing
7. Assignment on Signalling
8. Assignment on Digital Switching
9. Study of Digital Modulation: PSK, QPSK, QAM
10. Study of MODEM
11. Study of EPABX
12. Study of fibre optic communication
13. Study of source characteristics
14. Study of detector characteristics
15. Measurement of NA; attenuation.
16. Exp. Based on microwave bench.

6. IMPLEMENTATION STRATEGY (PLANNING) :

1. Teaching Plan
2. Minimum 12 practicals/assignments in the term
3. Industrial Visit for students

7. REFERENCE BOOKS :

Sr. No	Author	Title	Publishers & Address
1.	Wayne Tomasi	Advanced Electronic Communication	PHI International
2.	T. Vishwanathan	Telecommunication Switching System and Networks	PHI Pvt. Ltd.
3	Lee W.C.Y.	Mobile Cellular Telecommunication	McGraw Hill
4	Taub & Schilling	Communication System	Tata McGraw Hill
5	William Stallings	ISDN: An Introduction	Macmillan Publishing House
6	Raj Pandya	Mobile and Personal Communication System and Services	PHI
7	George Kennedy	Electronic Comm. Systems.	TMH

1.SUBJECT DETAILS :

Course: Diploma in Digital Electronics	Semester:VII
Subject: # Computer Networks	Code: DE-76
Group: A*	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination scheme and Maximum marks							
Theory Per Week	Practical Per Week		Paper			Th	SESSIONAL	T/W	Oral	TOTAL
			Hrs	NP	Mks					
4	2	06	03	01	100	80	20	25	50	175

3. RATIONALE:

Networking has evolved as a powerful solution to data sharing and decision making problems in recent times. With advent of new technologies and falling of prices of hardware it is possible to give a cost effective approach to networking. In addition internet is virtually become an inseparable part of our lives.

4. OBJECTIVE:

After Studying the subject student will be able to

- (i) Recognize the need of networking .
- (ii) Identify necessary hardware and software elements for any situation in real life.
- (iii) Operate and troubleshoot various types of networks.
- (iv) Organise an internet based application to run on a certain system with its own characteristics.

5. DETAILED CONTENTS:

SECTION-I

Chapter	Content	Marks	Hours
1.	Introduction and significance of Networking.	10	04

	Practice			
	1. To study client / server concepts			
	2. To study installation of NIC card and File Server			
2.	2.0 Hardware Requirements for networking like	16		06
	2.1 Copper Wires			
	2.2 Glass Fibers			
	2.3 Radio			
	2.4 Satellites			
	2.5 Microwave			
	2.6 Infrared			
	Practice			
	1. Study of network software command			
	2. Installation of Modem			
3.	Local Asynchronous Communication using (RS-232)	20		08
	Practice			
	1. Assignment on RS 232 C			
4.	Introduction to Long distance Communication	20		08
5.	5.0 Packet Transmission	16		06
	5.1 Packets, Frames, Error detection			
	5.2 LAN topologies			
	5.3 Extension of LANS, Introduction to WAN			
	5.4 Routing, Protocols & Layering.			
	Practice			
	1.Installation of client.			
	2.Study of network software command			
	3.Study of network security			
	<u>SECTION-II</u>			
6.	6.1. Internetworking concept, Architecture & Protocols	18		06
	6.2 Internet Protocol addresses Binding Protocol Addresses			

	6.3 IP Data grams & its forwarding		
	6.4 IP encapsulation, fragmentation, & reassemble.		
	Future of IP		
	Practice Assignment on Protocols		
7.	TCP Reliable Transport Service	20	06
	Practice: Assignment on TCP/IP		
8.	8.0 Network application	24	10
	8.1 Client-Server Interaction with example		
	8.2 The socket interface		
	8.3 Naming with the domain name system.		
	8.4 Electronic mail Representation & Transfer		
	8.5 File Transfer & Remote File Access		
	8.6 Word wide Web & Browsing		
	Practice: Practice on Internet		
9.	Network Management & Network security	20	10
	Practice		
	1. Study of printing under network software		
	2. Study of network system administrator		

PRACTICE:

1. To study client / server concepts.
2. To study installation of NIC card and File Server.
3. Study of network software. .
4. Installation of Modem.
5. Installation of client.
6. Study of network software command.
7. .Study of network security.
8. Assignment on Protocols.
9. Assignment on TCP/IP.
10. Practice on Internet.
11. Study of printing under network software.
12. Study of network system administrator.

Total 164 64

6. IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Minimum 12 practicals/assignments in the term
3. Industrial Visit for students

7. REFERENCE BOOKS :

Sr.No	Author	Title	Publishers & Address
1.	Andrew Tanenbaum	Computer Networks	PHI
2.	William Stallings	Computer Networks	PHI
3	D.E.Comer	Computer Networks & Internet	Pearson Education

1. SUBJECT DETAILS :

Course : Diploma in Digital Electronics	Semester :VII
Subject: # Project	Code: DE-70
Group: A*	Compulsory

2. TEACHING AND EXAMINATION SCHEME :

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs/week	Practical Hrs/week		Paper			Th	Sessional	T/W	Practical /Oral	Total
			Hrs	N P	Mks					
01	06	07	-	-	-	-	-	50	50	100

3. RATIONALE :

The project developed for any electronic application gives an in depth understanding of all the concepts learnt at the lower semesters. The students are also exposed to the various stages of making a project. It will enhance the design abilities in a student and the capability to work in a team.

4. OBJECTIVES :

After developing a project the students will be able to :

1. Design an electronic circuit
2. Design PCB
3. Test and calibrate the electronic equipment

4. To write the Project Report

5. Each student/group of students will submit detailed project report with following details

1. Schematic diagram
2. Working of the project and documented program
3. PCB – artwork
4. Assembly drawing
5. Front panel drawing
6. Specification sheet
7. List of components and cost

6. Basis of Marking :

Students will be marked on the basis of

1. Understanding and completeness of the Project
2. Approach to the solution of problem
3. Planning and implementation
4. Design and testing procedure
5. Project Report
6. Student's involvement in the Project

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: VII
Subject: Industrial Management – II	Code: ME-02
Group: M*	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination Scheme and Maximum Marks							
Theory Hrs. Per Week	Practical Hrs. Per Week		Paper			TH	Sessional	T/W	Pract.	Total
		Hrs	N/P	Mk s						
03	-	03	03	01	100	80	20	-	-	100

3. RATIONALE :

Management is the process of getting work done through work force to achieve the objectives of the organization. This subject “Industrial Management-II” covers the fundamentals principles, objectives, process and various processes and activities of management. Along with attaining the necessary technical knowledge, the technical students also require some inputs in management areas so as to enable them to carry out their work effectively and efficiently. This subject will enable the students to understand various processes and activities in an industrial organization.

4. OBJECTIVES :

6. To know the process of management.
7. To understand the organizational structure and its importance.
8. To know the various functional areas of an organization.
9. To understand the Basics of costing and finance.
10. To understand various functions of marketing.
11. To enable the students applying basic principles of management in their work life.

5. DETAILED CONTENTS:

SECTION-I

Chapter	Content	Marks	Hours
	Management		
1.	1.10 Introduction to management		
	1.11 Meaning, definition and importance.		
	1.12 Relevance of management to engineers.	10	06
	1.13 Principles of management.		

Functions of management

2.	2.1 Functions of management.		
	2.2 Planning and organizing.		
	2.3 Directing, communicating.	5	04
	2.4 Motivating and coordinating.		
	2.5 Controlling		

Organizational structures

3.	3.1 Meaning, types and factors determining structure.		
	3.2 Line organization.		
	3.3 Functional organization.	10	04
	3.4 Line and staff organization.		
	3.5 Project based organization.		

Procurement

4.	4.1 Purchase procedures.		
	4.2 Types of purchase.		
	4.3 Principles of purchase.	10	04
	4.4 Inventory and inventory control.		
	4.5 Duties of storekeeper.		

Human resource management

	5.1 Importance of human resources to industry.		
	5.2 Recruitment, selecting and placement of man-power.		
5.	5.3 Employee welfare measures in industry.	15	06
	5.4 Training and development.		
	5.5 Self-management.		
	5.6 Time management.		

SECTION – II

6.	Marketing	10	05
	6.1 Importance of marketing.		
	6.2 Sales and marketing difference.		
	6.3 Product life cycle and marketing strategies.		
	6.4 Channels of distribution.		
	6.5 Packaging and advertising.		
	6.6 Export marketing.		
7.	Finance	10	06
	7.1 Sources of finance.		
	7.2 Working capital and fixed capital.		
	7.3 Financial statements of a company.		
	7.4 Financial ratios.		
	7.5 Budgets and budgetary control.		

8.	Cost and cost calculation	15	06
	8.1 Objectives of cost calculation.		
	8.2 Classification of cost-bases.		
	8.3 Variable and fixed cost.		
	8.4 Direct and indirect cost.		
	8.5 Functional cost.		
	8.6 Cost control and cost reduction.		
	8.7 Overheads and types of overheads.		
	8.8 Cost calculation of a product.		
	8.9 Break even analysis.		
9.	Depreciation	7	03
	9.1 Meaning and importance.		
	9.2 Causes of depreciation.		
	9.3 Methods of calculation of depreciation.		
	9.4 Machine replacement factors to be considered.		
10.	Time value of money	08	04
	10.1 Equivalence.		
	10.2 Simple and compound interest.		
	10.3 Present worth method.		
	10.4 Future worth method.		
	10.5 Sinking fund method.		
	10.6 Capital recovery method.		

REFERENCE BOOKS :

Sr. No.	Author	Title	Publishers
1.	T R Banga & S C Sharma	Industrial Organization & Engineering Economics	Khanna Publications
2.	O P Khanna	Industrial Management	Dhanpatrai & Sons
3.	K C Jain & Agarwal	Production Planning Control & Industrial Management	Khanna Publications

1. SUBJECT DETAILS:

Course: Diploma in Digital Electronics	Semester: VII
Subject: #Robotics (Elective)	Code: IE -73
Group: A*	Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination scheme and Maximum marks							
Theory Per Week	Practical Per Week		Paper			Th	Sessional	T/w	oral	Total
		Hrs	NP	Mks						
4	2	06	03	01	100	80	20	25	50	175

3. RATIONALE:

This course enables students to know the fundamental & application of robotics & how to control industrial robot.

4. OBJECTIVES:

After studying the subject student will be able to:

1. Analyze & control industrial robot.
2. Write the specification of robotics
3. List applications of robots
4. Maintain & safely handle robots

5. DETAILED CONTENTS:

		SECTION-1	
Chapter	Contents :	Hours	Marks
1.	Robots parameters	06	18
	1.1 Concept of robotics		
	1.2 Role of robots in various manufacturing industries		
	1.3 Robots specification parameters		
	1.3.1 Stationary robots: range of operation,speed , repeatability,teaching method, number of controllable axes,external interface PLC function, programming capacity,dimensions,weight		
	1.3.2 Mobile robots: physical, constructional dimensions,power, mobility, swing radius ,software sensing and manipulation ,onboard computing electronics		
2.	Robot motion	14	32
	2.1 Robot classification: According to applications ,according to control systems		

2.2	Robot anatomy :		
	Polar configuration ,cylindrical configuration ,Cartesian configuration , joint arm configuration		
2.3	Robot motion :		
	Vertical transverse , radial transverse , rotational transverse , 2 ,3 & 4 degree of freedom , speed of motion ,and load carrying capacity , precision of movement : special resolution , accuracy , repeatability		
2.4	Robot drive systems :		
	hydraulic drives , electric drive , pneumatic drive.		
3.	Robot and effectors.	12	32
3.1	Gripper mechanism :		
	CAM actuated gripper , screw type gripper , magnetic gripper , adhesive grippers , hooks and other miscellaneous . devices .		
3.2	End effectors interface :		
	physical support , power and signal transmission , various consideration in gripper selection		
<i>SECTION-2</i>			
4.	Sensors in robot.	14	42
4.1	Sensing of physical parameters :		
	Types of sensors , tactile sensors , force / torque sensors , Proximity / range sensors .		
4.2	Machine vision :		
	function of machine vision (block diagram) illumination techniques , sending digital image , major steps of image compression , segmentation –thresholding techniques ,		

	multilevel thresholding , regions splitting , region merging , feature extraction thinning object recognition noise detection and removing (near neighbour techniques) .		
5.	Application of robotics : material transfer ,loading/ unloading , welding : spot welding and arc welding – assembly , spray coating , future application.	10	28
6.	Maintenance and safety:	8	12
6.1	Maintenance : Robot preventive maintenance, robot refurbishment, robot overhaul, sub assembly recondition and rebuild, onsite Maintenance.		
6.2	Safety in robots : work place design , safety sensors , safety monitoring.		
	Total	64	164

PRACTICE / ASSIGNMENT:

List of practicals:

1. To prepare a report of following points using searching a information through internet :
 1. Manipulator
 2. End effectors
 3. Link
 4. Joint
 5. robot motion
 6. Robot applications
2. To verify angular displacement of the shaft of the stepper motor using a microcontroller.
3. To verify the number of rotation of the shaft of steeper motor using microcontroller.
4. To control forward and backword movement of the object coupled with the shaft of dc motor using limit switch
5. To prepare report on comparision and applications of the various types of grippers mentioned in the syllabus after conducting of the group discussion of the following points types of gripper mechanism , application and effects of loading

6. To perform pick and place operation using magnetic operation to determine maximum load carrying capacity for the given magnetic gripper
7. To enlist various features available in MATLAB image processing tool bar and to read the digital image using MATLAB.
8. To perform a following operation on digital image using MATLAB software : edge detection ,segmentation
9. To determine various factors affecting on proximity using a proximity switch

6. IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Minimum 12 practicals/assignments in the term
3. Industrial Visit for students

7. REFERENCE BOOKS:

Sr no.	Author	Title	Publisher
1.	Mikell P Groove	Industrial Robotics	McGraw Hill
2.	R. J. Shilling	Fundamentals of Robotics	Prentice hall of India
3.	R. D. Klafter	Robotics Engineering	Prentice Hall of India

Web References:

1. www.robotics.com
2. www.robots-epson.com
3. www.motoman.com

1. SUBJECT DETAILS :

Course: Diploma in Digital Electronics
Subject: #Embedded System Design (Elective)
Group: A*

Semester : VII
Code : DE-79
Compulsory

2. TEACHING AND EXAMINATION SCHEME:

Teaching Scheme		Credits	Examination scheme and Maximum marks							
Theory Per Week	Practical Per Week		Paper			Th	SSL	T/W	Oral	TOTAL
			Hrs	NP	Mks					
4	2	06	03	01	100	80	20	25	50	175

3. RATIONALE:

This course enables students to know the devices, buses & also learn the embedded design of microcontroller.

It helps student to get thorough explanation of embedded hardware architecture, interfacing techniques, buses and protocols, hardware and software interrupt, embedded software programming, modeling, inter-process synchronization and real time operating system.

4. OBJECTIVES:

After studying this subject student will be able to

1. Define Embedded system
2. Embedded system design of microcontroller
3. Case studies of Smart card, Robots, Automobile control, Mobile phone, Process control

5. DETAILED CONTENTS:

SECTION-I

Chapter Content	Hrs	Marks
(1) Introduction to Embedded Systems	04	10
(2) Embedded Microcontrollers : ARM7TDMI 32-Bit microcontroller architecture and instruction set - Memory organization Memory selection for an embedded system. -Dynamic Memory Access. -Interfacing of memory and processor -Embedded mass storage and FAT file system.	20	50
(3) Devices And Buses : I/o devices - Timer and Counting devices. - USB, CAN, RS232, RS485. - I ² c buses. - I/o devices : Keypad, Display interface (LCD, Touchscreen, Graphic devices)	08	22
(4) Device Drivers and Interrupt Servicing Mechanism : Device Drivers -Parallel port drivers -Serial port drivers -Interrupt servicing -Multiple Interrupts -Deadline and Interrupt latency		

(5) **Programming And Testing**

- Assembly Level Programming
- Cross Compilers
- IDE & Source Level Debugger.

(6) **Real time operating system**

- Operating system services
- Process management
- Timer Functions
- Event Functions
- Memory management
- Device file and I/o subsystem management
- Interrupt routines in RTOS environment

(7) **Case Studies** : CODEC, Smart card, Robots, Automobile control,
Mobile phone, Process control.

Practice:

1. Programs based on ARM7TDMI microcontroller
2. Interfacing programs on serial port, parallel port
3. Program on interrupt
4. Study of RTOS
5. Assignment on Case Studies

6. IMPLEMENTATION STRATEGY (PLANNING):

1. Teaching Plan
2. Minimum 12 practicals/assignments.
3. Industrial Visit for students

7. REFERENCE BOOKS :

Sr. No	Author	Title	Publishers & Address
1.	Rajkamal	Embedded Systems – Architecture Programming & Design	Tata McGraw Hill
2.	David Simon	An Embedded Software	Pearson Education
3.	Technical Reference Manual of ARM7TDMI	–	–

Web References:

1. www.intel.com
2. www.goemdedded.com
3. www.arm.com

w.e.f.-Batch admitted June,2008 (Progressively)

Shri Vile Parle Kelavani Mandal`s
SHRI BHAGUBHAI MAFATLAL POLYTECHNIC

DISCIPLINE: DIGITAL ELECTRONICS FULL TIME

TEACHING & EXAMINATION SCHEMES

VIII SEMESTER

Sr No	Subject Duration	Credit		Weekly Report	Quiz Test Marks	Dissertation (Report)	Oral/ Viva	Total
8.1	26 Weeks	15	Maximum Marks	50	50	50	50	200
			Minimum Marks	20	20	20	20	80

- 1 Maximum 100 marks of Inplant Training will be considered for Award of Diploma.**
- 2 The Examination shall be conducted for 200 marks and same marks will be converted in to 100 by the Examination Section.**